

Certificate of Training

ARPIT RAJ

from Shri Mata Vaishno Devi University has successfully completed an 8-week online training on **VLSI Design**. The training consisted of Introduction to VLSI, HDL Coding Concepts, Combinational Circuits, Sequential Circuit Design, Finite State Machines, System Design using FPGA, and Case Studies & Final Project modules.

We wish ARPIT all the best for future endeavours.

A handwritten signature in black ink, appearing to read "Sarvesh".

Sarvesh Agrawal
FOUNDER & CEO, INTERNSHALA



Dr. Shankar Raman
CEO, IITM PRAVARTAK TECHNOLOGIES FOUNDATION

Date of certification: 2025-08-09

Certificate no. : bwe4n7xyjtt

For certificate authentication, please visit https://trainings.internshala.com/verify_certificate

CERTIFICATE

This is to certify that

Ms./Mr. ARPIT RJ

Son/Daughter of UPENDRA KUMAR SINGH Student ID CAN_37672862

has successfully completed the VLSI DESIGN Course

with Grade A from SCHOLIVERSE EDUCARE PRIVATE LIMITED

Date of Issuance: 9th August 2025

Certificate ID: d08rvlp80q5n0kwl



Issued by

Sarvesh Madhu Agrawal

Founder & CEO

**SCHOLIVERSE EDUCARE PRIVATE
LIMITED**