## B.Tech-3rd(EEE) Analog Electronic Circuit

Full Marks: 50

Time:  $2\frac{1}{2}$  hours

Answer all questions

The figures in the right-hand margin indicate marks

Symbols carry usual meaning

Any supplementary materials to be provided

1. Answer all questions:

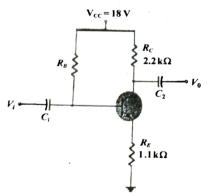
- $2 \times 5$
- What is operating point of an amplifier?
  Write its significance.
- (b) What is stability factor?
- (c) What is pinch of voltage?
- (d) Draw the Darlington circuit.
- (e) State and explain Barkhausen's criteria.

- 2. (a) What is biasing? Explain Emitter biasing with neat sketch.
  - (b) Explain the output characteristic of CE transistor. Draw its load line.

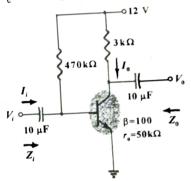
Or

- (a) Discuss voltage divider bias configuration of BJT amplifier using exact method and approximate method with suitable circuit diagram.
- (b) Determine  $I_C V_{CE}$  and suitable  $I_{CQ}$  and  $V_{CEQ}$ . Given  $R_B$  430 k $\Omega$ .

(Continued)



(a) Determine the following for the above circuit. Given r₀ = ∞
 (i) re, (ii) Av, (iii) Z₀, (iv) Zᵢ

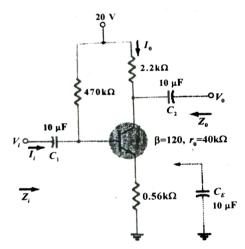


- (b) Determine the following for the above circuit. Given r₀ = ∞
   (i) r₀, (ii) A₀, (iii) Z₀, (iv) Zᵢ
  - $\begin{array}{c|c}
    22 \text{ V} \\
    \hline
    I_0 \\
    \hline
    I_1 \\
    \hline
    Z_i
    \end{array}$   $\begin{array}{c|c}
    6.8k\Omega \\
    \hline
    10 \mu F \\
    \hline
    V_0 \\
    \hline
    10 \mu F \\
    \hline
    8.2k\Omega \\
    \hline
    1.5k\Omega \\
    \end{array}$   $\begin{array}{c|c}
    70 \mu F \\
    \hline
    20 \mu F \\
    \end{array}$

Or

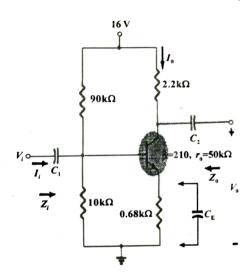
(a) Determine the following for the above circuit. Given  $r_0 = \infty$ 

 $(i) r_e, (ii) A_v, (iii) Z_0, (iv) Z_i$ 



(b) Determine the following for the above circuit. Given  $r_0 = \infty$  4

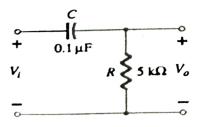
(i)  $r_e$ , (ii)  $A_v$ , (iii)  $Z_0$ , (iv)  $Z_i$ 



- (a) How FET differs from BJT? Explain Self Bias circuit for Depletion type MOSFET.
  - (b) Sketch the transfer characteristics for an p-channel depletion type MOSFET with  $I_{DSS} = 10 \text{mA}$  and  $V_p = -4 \text{V}$ .

Or

- (a) Discuss hybrid-π common emitter transistor model.
- (b) Discuss a.c. analysis of JFET amplifier fixed bias configuration.
- (a) Explain the frequency response of RC coupled amplifier. Compare it with that of transformer coupled and direct -coupled amplifiers.
  - (b) Determine the break frequency and gain at  $A_{v(dB)} = -6dB$ . Sketch the frequency response curve showing asymptotes and -3dB point.



Or

Demonstrate Miller effect capacitance for an inverting amplifier.

6. Explain the concept of positive feedback. With a neat diagram discuss various oscillator circuits.

Or

How do you differentiate between voltage amplifier and power amplifier? Compare class A and Class B amplifiers.