

Date:- 11 June, 2008

Data Sheet Issue:- 2

Provisional Data Avalanche Diode Type W3270N#22A

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V _{RRM}	Repetitive peak reverse voltage, (note 1)	2200	V
V _{RSM}	Non-repetitive peak reverse voltage, (note 1)	2300	V
P _{RSM}	Maximum surge reverse power dissipation, (note 2)	100	kW

	OTHER RATINGS	MAXIMUM LIMITS	UNITS
I _{F(AV)M}	Maximum average forward current, T _{sink} =55°C, (note 3)	3239	А
I _{F(AV)M}	Maximum average forward current. T _{sink} =85°C, (note 3)	2670	А
I _{F(AV)M}	Maximum average forward current. T _{sink} =85°C, (note 4)	1630	А
I _{F(RMS)M}	Nominal RMS forward current, T _{sink} =25°C, (note 3)	5887	А
I _{F(d.c.)}	D.C. forward current, T _{sink} =25°C, (note 5)	5015	А
I _{FSM}	Peak non-repetitive surge t _p =10ms, V _{rm} =60%V _{RRM} , (note 6)	27.6	kA
I _{FSM2}	Peak non-repetitive surge t _p =10ms, V _{rm} ≤10V, (note 6)	30.4	kA
l ² t	$I^{2}t$ capacity for fusing t _p =10ms, V _{rm} =60%V _{RRM} , (note 6)	3.81 × 10 ⁶	A ² s
l ² t	$I^{2}t$ capacity for fusing t_{p} =10ms, V_{rm} ≤10V, (note 6)	4.62 × 10 ⁶	A ² s
T _{j op}	Operating temperature range	-55 to +175	°C
T _{stg}	Storage temperature range	-55 to +200	°C

Notes:-

1) De-rating factor of 0.13% per °C is applicable for T_j below 25°C.

2) In accordance with IEC60747-2 clause 7.3.3.1 (Triangular waveform pulse method), $t_p=3\mu s$.

3) Double side cooled, single phase; 50Hz, 180° half-sinewave.

4) Single side cooled, single phase; 50Hz, 180° half-sinewave.

5) Double side cooled.

6) Half-sinewave, 175°C T_j initial.

Characteristics

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS
V _{FM}	Maximum peak forward voltage	-	-	1.52	I _{TM} =6400A	V
V_{FM}	Maximum peak forward voltage	-	-	1.84	I _{TM} =9800A	V
V _{T0}	Threshold voltage	-	-	0.818		V
r _T	Slope resistance	-	-	0.108		mΩ
I _{RRM}	Peak reverse current	-	-	50	Rated V _{RRM}	mA
I _{RRM}	Peak reverse current	-	-	50	Rated V _{RRM} , T _j =25°C	mA
Q _{rr}	Recovered charge	-	3000	3450		μC
Q _{ra}	Recovered charge, 50% Chord	-	2300	-	I _{TM} =1000A, t _p =1000µs, di/dt=10A/µs,	μC
l _{rr}	Reverse recovery current	-	160	-	V _r =50V	А
t _{rr}	Reverse recovery time	-	29	-		μs
-		-	-	0.022	Double side cooled	K/W
R _{thJK}	Thermal resistance, junction to heatsink	-	-	0.044	Single side cooled	K/W
F	Mounting force	19	-	26	Note 2	kN
Wt	Weight	-	510	-		g

Notes:-

Unless otherwise indicated T_j=175°C.
For other clamp forces, please consult factory.

Notes on rupture rated packages. This product is available with a non-rupture rated package. For additional details on these products, please consult factory.

Notes on Ratings and Characteristics

1.0 Voltage Grade Table

Voltage Grade	V _{RRM}	V _{RSM}	V _R
	V	V	DC V
22	2200	2300	1350

2.0 Extension of Voltage Grades

This report is applicable to other voltage grades when supply has been agreed by Sales/Production.

3.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for T_i below 25°C.

4.0 Snubber Components

When selecting snubber components, care must be taken not to use excessively large values of snubber capacitor or excessively small values of snubber resistor. Such excessive component values may lead to device damage due to the large resultant values of snubber discharge current. If required, please consult the factory for assistance.

A T

5.0 Computer Modelling Parameters

5.1 Device Dissipation Calculations

Where V_{T0} =0.818V, r_T=0.108m Ω ,

 R_{th} = Supplementary thermal impedance, see table below and

ff = Form factor, see table below.

Supplementary Thermal Impedance						
Conduction Angle	6 phase (60°)	3 phase (120°)	1/2 wave (180°)	d.c.		
Square wave Double Side Cooled	0.0285	0.0255	0.0240	0.0220		
Square wave Single Side Cooled	0.0513	0.0484	0.0469	0.0440		
Sine wave Double Side Cooled	0.0257	0.0233	0.022			
Sine wave Single Side Cooled	0.0482	0.0463	0.044			

Form Factors						
Conduction Angle6 phase (60°)3 phase (120°)½ wave (180°)d.c						
Square wave	2.449	1.732	1.414	1		
Sine wave	2.778	1.879	1.57			

5.2 Calculating V_F using ABCD Coefficients

The on-state characteristic I_F vs. V_F , on page 6 is represented in two ways;

- (i) the well established V_{T0} and r_T tangent used for rating purposes and
- (ii) a set of constants A, B, C, D, forming the coefficients of the representative equation for V_F in terms of I_F given below:

$$V_F = A + B \cdot \ln(I_F) + C \cdot I_F + D \cdot \sqrt{I_F}$$

The constants, derived by curve fitting software, are given below for both hot and cold characteristics. The resulting values for V_F agree with the true device characteristic over a current range, which is limited to that plotted.

	25°C Coefficients	175°C Coefficients
А	1.629038	0.7019069
В	-0.1913977	-0.02419013
С	-6.288E-05	4.58848E-05
D	0.02409212	0.009205413

5.3 D.C. Thermal Impedance Calculation

$$r_t = \sum_{p=1}^{p=n} r_p \cdot \left(1 - e^{\frac{-t}{\tau_p}} \right)$$

Where p = 1 to n, n is the number of terms in the series and:

- t = Duration of heating pulse in seconds.
- r_{t} = Thermal resistance at time t.
- $r_p =$ Amplitude of p_{th} term.
- τ_p = Time Constant of r_{th} term.

The coefficients for this device are shown in the tables below:

	D.C. Single Side Cooled							
Term	Term 1 2 3 4 5							
r _p	0.0291698	4.295845×10 ⁻³	7.57109×10 ⁻³	2.195801×10 ⁻³	1.628753×10 ⁻³			
τρ	5.67822	1.123602	0.1407857	0.014381914	1.272749×10 ⁻³			

D.C. Double Side Cooled							
Term	rm 1 2 3 4						
r _p	0.01177146	6.485814×10 ⁻³	2.471007×10 ⁻³	1.607109×10 ⁻³			
τρ	0.9495346	0.1337950	0.01636628	1.255571×10 ⁻³			

6.0 Reverse recovery ratings

(i) Q_{ra} is based on 50% I_{rm} chord as shown in Fig. 1

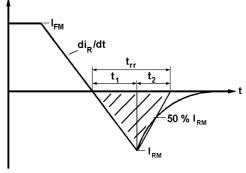


Fig. 1

(ii) Q_{rr} is based on a 150 μ s integration time i.e.

$$Q_{rr} = \int_{0}^{150\mu s} i_{rr}.dt$$

(iii)

K Factor =
$$\frac{t_1}{t_2}$$

Curves

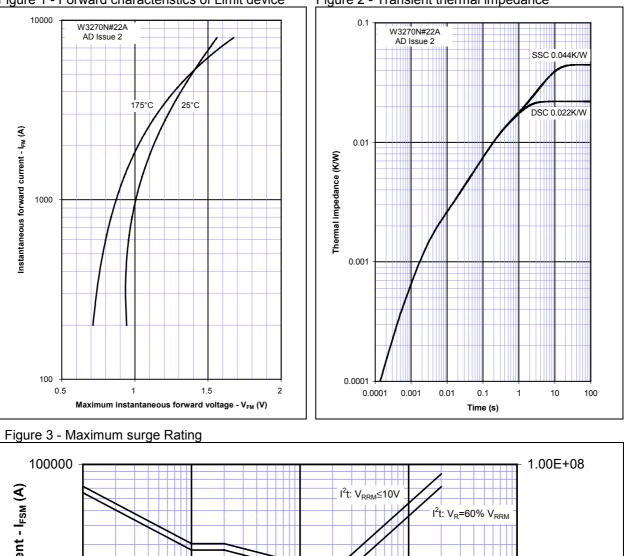
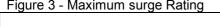


Figure 1 - Forward characteristics of Limit device

Figure 2 - Transient thermal impedance



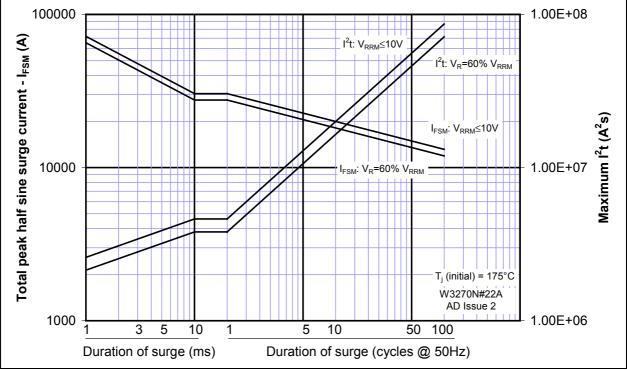
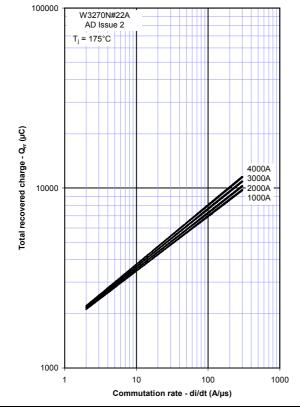
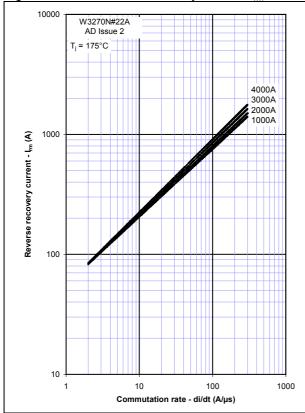
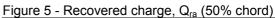


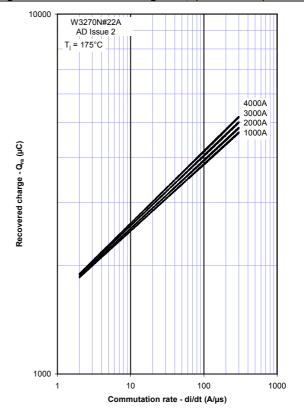
Figure 4 - Total recovered charge, Qrr



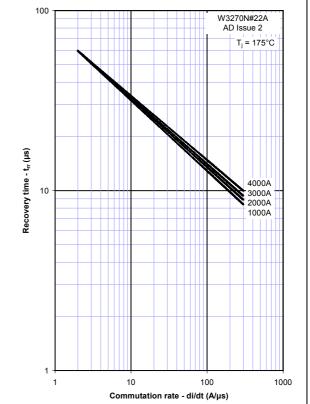












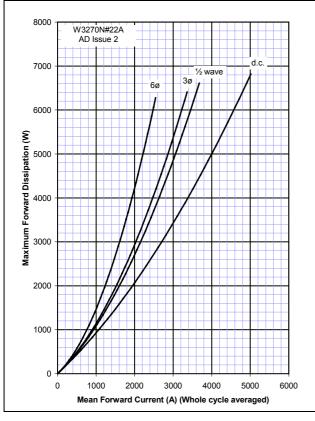
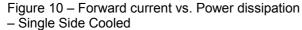
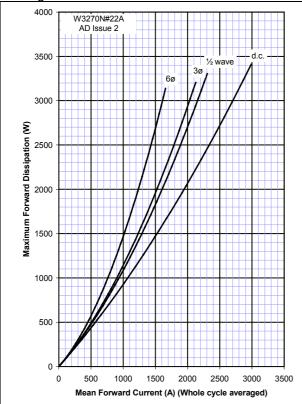
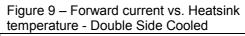
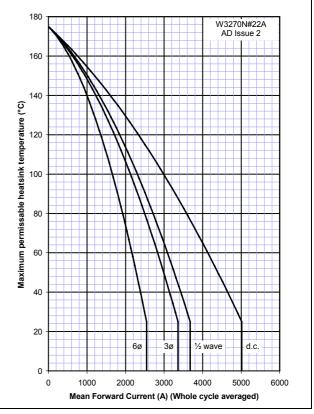


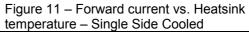
Figure 8 – Forward current vs. Power dissipation – Double Side Cooled

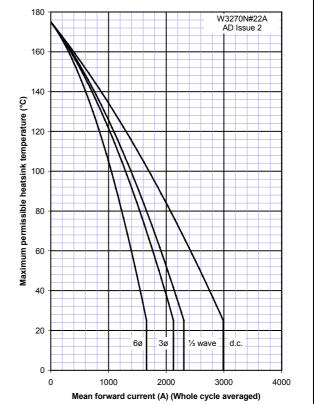












Outline Drawing & Ordering Information

