

Current Transducer CTSR 0.3-P/SP11

I_{PRN} = 300 mA

For the electronic measurement of current: DC, AC, pulsed..., with galvanic separation between the primary and the secondary circuit.





Features

- Closed loop (compensated) current transducer
- Voltage output
- Single supply voltage
- · PCB mounting.

Special features

- Different pin out
- No retention pin
- · Extending measuring range
- Test winding included.

Advantages

- High accuracy
- · Very low offset drift over temperature
- Wide aperture
- High overload capability
- · High insulation capability
- Reference pin with two modes, Ref In and Ref Out
- Degauss and test functions.

20August2015/version 7

Applications

- · Residual current measurement
- Leakage current measurement in transformerless PV inverters
- First human contact protection of PV arrays
- Failure detection in power sources
- Symmetrical fault detection (e.g. after motor inverter)
- · Leakage current detection in stacked DC sources
- Single phase or three phase nominal current measurement up to ±30 A per wire (DC or AC).

Standards

EN 50178: 1997IEC 61010-1: 2010UL 508: 2010.

Application Domain

- Industrial
- Suitable to fulfil VDE 0126-1-1, UL 1741 and IEC 62109-2.

N°97.G9.A2.011.0



Absolute maximum ratings

Parameter	Symbol	Unit	Value
Supply voltage	$U_{\rm c}$	V	7
Primary conductor temperature	$T_{_{\mathrm{B}}}$	°C	110
Overload capability (100 µs, 500 A/µs)	$\hat{I}_{_{P}}$	А	3300
Voltage between test winding and secondary pins	$U_{_{\mathrm{d}}}$	V	35
Current of test winding	$I_{\scriptscriptstyle au}$	mA	300

Stresses above these ratings may cause permanent damage. Exposure to absolute maximum ratings for extended periods may degrade reliability.

UL 508: Ratings and assumptions of certification

File # E189713 Volume: 2 Section: 3

Standards

- CSA C22.2 NO. 14-10 INDUSTRIAL CONTROL EQUIPMENT Edition 11 Revision Date 2011/08/01
- UL 508 STANDARD FOR INDUSTRIAL CONTROL EQUIPMENT Edition 17 Revision Date 2010/04/15

Ratings

Parameter	Symbol	Unit	Value
Primary involved potential*		V AC/DC	600
Max surrounding air temperature	T _A	°C	105
Primary current	I_{P}	А	30
Secondary supply voltage	U _c	V DC	5
Output voltage	V_{out}	V	0 to 5

^{*} Environmental: For use in Pollution degree 3.

Conditions of acceptability

When installed in the end-use equipment, consideration shall be given to the following:

- 1 A suitable enclosure shall be provided in the end-use application.
- 5 CTSR series is intended to be mounted on the printed wiring board of the end-use equipment.
- 7 The uninsulated live parts of primary feeder and secondary circuit clearance spacing of Model CTSR XX-P series shall maintain at least 5.5 mm apart.
- 8 Primary feeder of the devices shall be connected after an overvoltage device or system which has been evaluated by the Standard for Transient Voltage Surge Suppressors, UL 1449.

Marking

Only those products bearing the UL or UR Mark should be considered to be Listed or Recognized and covered under UL's Follow-Up Service. Always look for the Mark on the product.



Insulation coordination

Parameter	Symbol	Unit	Value	Comment
Rms voltage for AC insulation test, 50 Hz, 1 min	$U_{_{ m d}}$	kV	5.4	
Impulse withstand voltage 1.2/50 μs	Û _w	kV	10.1	
Partial discharge extinction rms voltage @ 10 pC	$U_{ m e}$	kV	1.65	
Clearance (pri sec.)	d _{CI}	mm	11	Shortest distance through air
Creepage distance (pri sec.)	d _{Cp}	mm	11	Shortest path along device body
Comparative tracking index	CTI		600	
Application example	-	>	1000	Reinforced insulation, CAT III, PD2 non uniform field according to EN 50178
Application example	-	٧	600	Reinforced insulation, CAT III, PD3 non uniform field according to EN 50178, IEC 61010

Environmental and mechanical characteristics

Parameter	Symbol	Unit	Min	Тур	Max	Comment
Ambient operating temperature	T_{A}	°C	-40		105	
Ambient storage temperature	T_{s}	°C	-50		105	
Mass	т	g		28		



Electrical data

At T_A = 25 °C, U_C = +5 V, output voltage referred to V_{ref} unless otherwise noted (see Min., Max., typical definition paragraph) in page 8.

Parameter	Symbol	Unit	Min	Тур	Max	Comment
Primary nominal residual rms current	$I_{\scriptscriptstyle{PRN}}$	mA		300		
Primary residual current, measuring range	I_{PRM}	mA	-850		850	
Supply voltage	U _c	V	4.75	5	5.25	
Current consumption	$I_{\mathtt{C}}$	mA		17.5	21.6	$+I_{\rm P} ({\rm mA})/N_{\rm S}$ With $N_{\rm S}$ = 1000 turns -40 105 °C
Output voltage referred to GND (during Degauss cycle)	V _{out}	V		0.3	0.5	Note 1)
Output voltage referred to V_{ref} (Test current)	$V_{ m out}$	V	0.4	0.75	1.1	Note 1)
Reference voltage @ I_P = 0	V _{ref}	V	2.495	2.5	2.505	Internal reference
External reference voltage	$V_{ m ref}$	V	2.3		4	Internal reference of V_{ref} input = 499 Ω Note 1)
Electrical offset current referred to primary (Note 2)	I_{OE}	mA	-24	4.2	24	
Temperature coefficient of V_{ref}	TCV _{ref}	ppm/K			±50	−40 105 °C
Temperature coefficient of $V_{\rm OE}$ @ $I_{\rm P}$ = 0	TCV _{OE}	ppm/K			±520	ppm/K of 2.5 V -40 105 °C
Theoretical sensitivity	G _{th}	V/A		2.476		
Sensitivity error (Note 2)	ε _G	%	-0.7	0.3	0.7	$R_{\rm L}$ > 500 k Ω
Sensitivity error with testing winding (±3 %)	ε _G	%	-3		+3	$R_{\rm L}$ > 500 k Ω
Temperature coefficient of G	TCG	ppm/K			±100	−40 105 °C
Linearity error	\mathcal{E}_{L}	% of $I_{\scriptscriptstyle \mathrm{PRM}}$		0.4	1.3	
Number of turns (test winding)	N _T			20		
Test current, measuring range	I_{\scriptscriptstyleTM}	mA	-42.5		42.5	
Magnetic offset current (1000 × $I_{\rm PRN}$) referred to primary	$I_{\scriptscriptstyle{OM}}$	mA		17		
Output rms noise voltage (1 Hz 10 kHz)	V _{no}	mV		4		$R_{\rm L}$ > 500 k Ω
Reaction time @ 10 % of $I_{\rm PRN}$	t _{ra}	μs		5		$R_{\rm L}$ > 500 k Ω , di/dt > 5 A/ μ s
Step response time to 90 % of $I_{\mbox{\tiny PN}}$	t _r	μs		30		$R_{\rm L}$ > 500 k Ω , di/dt > 5 A/ μ s
Frequency bandwidth (-1 dB)	BW	kHz		9.5		$R_{\rm L}$ > 500 k Ω
Accuracy (Note 3)	Х	%			1.5	$= (\varepsilon_{\rm G}^2 + \varepsilon_{\rm L}^2)^{1/2}$

¹⁾ See "Application information" section. Notes:

²⁾ Only with a primary nominal residual current, see paragraph "Primary nominal residual current and primary nominal

³⁾Accuracy @ T_A and I_P : $X_{TA} = (X^2 + (TCG \times 100 \times (T_A - 25))^2 + (TCV_{OE} \times 2.5 \times (T_A - 25)/G_{th} \times 100/I_P)^2)^{1/2}$.

2015/version 7 LEM reserves the right to carry out modifications on its transducers, in order to improve them, without prior notice



Typical performance characteristics

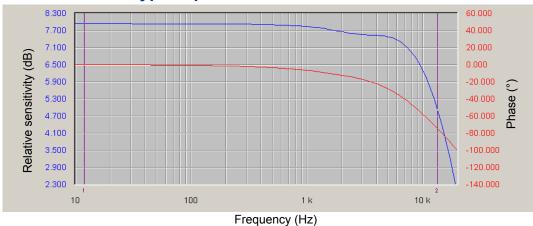
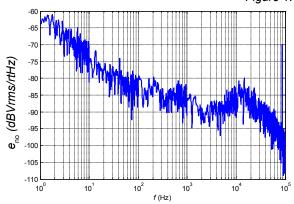


Figure 1: Frequency response



10²
10³
2 10⁴
10⁵
10⁶
10¹
10¹
10²
10¹
10¹
10²
10³
10⁴
10

Figure 2: Output noise voltage spectral density

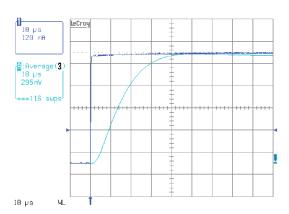


Figure 3: Output noise voltage, cumulated rms

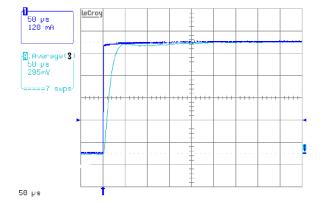


Figure 4: Typical step response

Figure 5: Typical step response



Application information

Filtering, decoupling CTSR transducer

Supply voltage U_c (5 V)

The CTSR transducers have internal decoupling capacitors, but in the case of a power supply track on the application PCB having a high impedance, it is advised to provide local decoupling, 100 nF or more, located close to the transducer.

Reference V_{ref}

Ripple present on the $V_{\rm ref}$ pin can be filtered with a low value of capacitance because of the internal 499 ohm series resistance. The CTSR transducers have an internal capacitor of 22 nF between $V_{\rm ref}$ pin and Gnd pin and the maximum filter capacitance value which could be added is 1 μ F. Adding a larger decoupling capacitor will increase the activation delay of degauss.

Output V_{out}

The CTSR transducers have an internal low pass filter 470 ohm/22 nF; if a decoupling capacitor is added on V_{out} pin, the bandwidth and the response time will be affected. In case of short circuit, the transducer CTSR can source or sink up to a maximum of 10 mA on its output V_{out} .

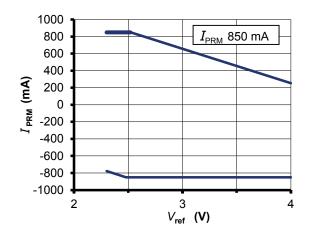
Using an external reference voltage

If the V_{ref} pin of the transducer is not used it could be either left unconnected or filtered according to the previous paragraph "Reference V_{ref} ".

The $V_{\rm ref}$ pin has two modes Ref out and Ref In:

- In the Ref out mode the 2.5 V internal precision reference is used by the transducer as the reference point for bipolar
 measurements; this internal reference is connected to the V_{ref} pin of the transducer through a 499 ohms resistor. It
 tolerates sink or source currents up to ± 5 mA, but the 499 ohms resistor prevents this current to exceed these limits.
- In the Ref In mode, an external reference voltage is connected to the V_{ref} pin; this voltage is specified in the range 2.3 to 4 V and is directly used by the transducer as the reference point for measurements. The external reference voltage V_{ref} must be able:
 - o either to source a typical current of $\frac{Vref 2.5}{499}$, the maximum value will be 3 mA when $V_{ref} = 4 \text{ V}$.
 - o or to sink a typical current of $\frac{2.5-Vref}{499}$, the maximum value will be 0.4 mA when V_{ref} = 2.3 V.

The following graphs show how the measuring range of the transducer depends on the external reference voltage value V_{ref} ($U_c = 5 \text{ V}$).





Primary nominal residual current and primary nominal current

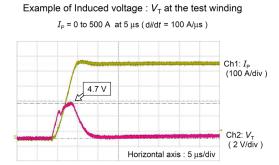
The primary nominal residual current is the sum of the instantaneous values of all currents flowing through the aperture of the transducer. The primary nominal current is the current flowing through any conductor placed into the aperture of the transducer. The presence of a primary nominal current DC or AC leads to an additional uncertainty. For example, with a primary nominal current of 30 A the uncertainty referred to primary is typical 4.3 mA.

Test winding

A test winding is wound around the compensation winding. It allows simulating a primary residual current to test the function of the transducer. The output voltage V_{out} referred to V_{ref} for a test current I_{T} is below. $V_{\text{out}} - V_{\text{ref}} = G_{\text{th}}^* I_{\text{T}}$ (test current) * 20

$$V_{\text{out}} - V_{\text{ref}} = G_{\text{th}}^* I_{\text{T}} \text{ (test current) * 20}$$

To fullfill the standard IEC 62109-2 with the transducer, the test winding must be used to verify the accuracy of the transducer according to clause 4.4.4.15.1 and 4.8.3.5 of the IEC 62109-2 before each attempted re-start of the PV inverter. The current injected in the test winding should be generated by a current source. When the test winding is not used, it must stay open. A high voltage may be generated by the test winding when a fast transient primary current is applied to the transducer (transformer effect); an additional protection is recommended in application PCB assembly if there is such a possibility.



CTSR transducer in Test mode

When the V_{ref} pin is forced at a low level voltage between 0 and 1 V and is maintained at this level, the output voltage V_{out} of CTSR transducer exhibits a fixed value (see specification) as if it measured a primary test current. The activation time of test mode is min 30 ms. The CTSR transducer can be maintained in test mode as long as needed for checking that it is fully operating.

CTSR transducer in Degauss mode

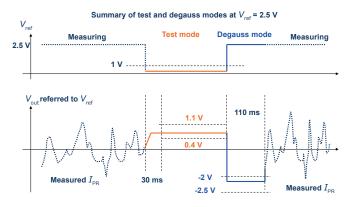
The CTSR transducers go in degauss mode automatically at each power on or on demand by using the V_{ref} pin. At power on:

A degauss is automatically generated at each power on of the CTSR transducer; during degaussing the output voltage V_{out} is maintained at 0.3 V typ. (max 0.5 V). After c.a. 110 ms, the output voltage V_{out} is released and takes the normal operation level in relation with the measured primary current.

Using V_{ref} pin:

When the pin V_{ref} is released from the Low level voltage defined in the Test mode above, there is a rising edge on V_{ref} which generates an automatic degauss.

The activation of degauss takes typically 40 μ s after releasing V_{ref} pin, then degauss lasts typically 110 ms.

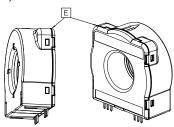




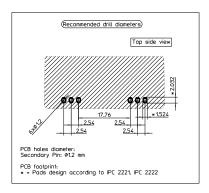
Isolation around the CTSR transducer housing

Due to the joint between the case and the cover of the CTSR transducer, there is some isolation distance to respect when primary conductors pass around the CTSR housing.

The figure below shows the joint and the apertures where the clearance between the secondary part inside the CTSR transducer and the surface of the housing is 3 mm (label E).



PCB footprint



Assembly on PCB

Recommended PCB hole diameter
 Maximum PCB thickness
 2.4 mm

Wave soldering profile maximum 260 °C, 10 s
 No clean process only

Consequently any primary conductor must be placed in the PCB at the requested isolation distance regarding these secondary sides (see also paragraph "Isolation around the CTSR transducer housing").

Definition of typical, minimum and maximum values

Minimum and maximum values for specified limiting and safety conditions have to be understood as such as well as values shown in "typical" graphs. On the other hand, measured values are part of a statistical distribution that can be specified by an interval with upper and lower limits and a probability for measured values to lie within this interval.

Unless otherwise stated (e.g. "100 % tested"), the LEM definition for such intervals designated with "min" and "max" is that the probability for values of samples to lie in this interval is 99.73 %. For a normal (Gaussian) distribution, this corresponds to an interval between -3 sigma and +3 sigma. If "typical" values are not obviously mean or average values, those values are defined to delimit intervals with a probability of 68.27 %, corresponding to an interval between -sigma and +sigma for a normal distribution. Typical, maximal and minimal values are determined during the initial characterization of a product.

Safety

This transducer must be used in limited-energy secondary circuits according to IEC 61010-1.



This transducer must be used in electric/electronic equipment with respect to applicable standards and safety requirements in accordance with the manufacturer's operating instructions.

Caution, risk of electrical shock



When operating the transducer, certain parts of the module can carry hazardous voltage (eg. primary busbar, power supply). Ignoring this warning can lead to injury and/or cause serious damage.

This transducer is a build-in device, whose conducting parts must be inaccessible after installation.

A protective housing or additional shield could be used.

Main supply must be able to be disconnected.

Page 8/9



Dimensions (in mm, general tolerance ±0.3 mm)

