

FEATURES

- Soft Punch Through Silicon
- 10µs Short Circuit Withstand
- Isolated MMC Base plate with AlN Substrate
- High Thermal Cycling Capability

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Auxiliaries

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM200PHM33-F000 is a half bridge 3300V, soft punch through n channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus full 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM200PHM33-F000

Note: When ordering, please use the whole part number.

KEY PARAMETERS

V_{CES}		3300V
$V_{CE(sat)}$	(typ)	2.8V
I_C	(max)	200A
$I_{C(PK)}$	(max)	400A

*(measured at the auxiliary terminals)

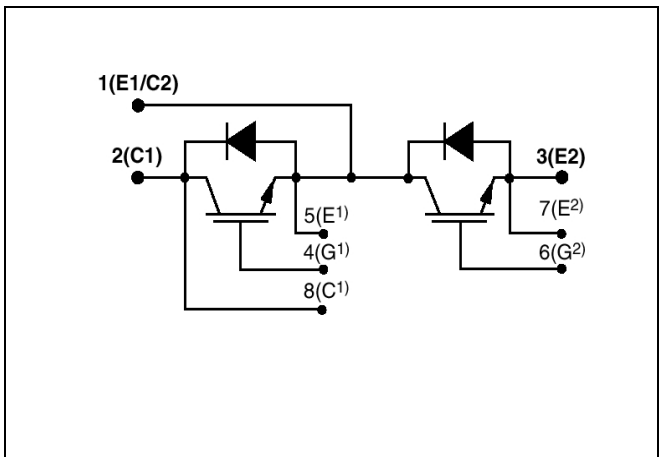


Fig. 1 Half Bridge circuit diagram



Fig. 2 Package

ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V _{CES}	Collector-emitter voltage	V _{GE} = 0V, T _j = -25°C	3300	V
V _{GES}	Gate-emitter voltage		±20	V
I _C	Continuous collector current	T _{case} = 90°C	200	A
I _{C(PK)}	Peak collector current	1ms, T _{case} = 115°C	400	A
P _{max}	Max. transistor power dissipation	T _{case} = 25°C, T _j = 150°C	2.6	kW
I ² t	Diode I ² t value	V _R = 0, t _p = 10ms, T _j = 125°C	20	kA ² s
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	6000	V
Q _{PD}	Partial discharge – per module	IEC1287. V ₁ = 3500V, V ₂ = 2600V, 50Hz RMS	10	pC

THERMAL AND MECHANICAL RATINGS

Internal insulation material: AIN
 Baseplate material: AISiC
 Creepage distance: 33mm
 Clearance: 20mm
 CTI (Critical Tracking Index): 350

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$R_{th(j-c)}$	Thermal resistance – transistor (per switch)	Continuous dissipation – junction to case	-	-	48	°C/kW
$R_{th(j-c)}$	Thermal resistance – diode (per switch)	Continuous dissipation – junction to case	-	-	96	°C/kW
$R_{th(c-h)}$	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	16	°C/kW
T_j	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	125	°C
T_{stg}	Storage temperature range	-	-40	-	125	°C
-	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M5	-	-	4	Nm

ELECTRICAL CHARACTERISTICS
 $T_{case} = 25^{\circ}\text{C}$ unless stated otherwise.

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units	
I_{ces}	Collector cut-off current	$V_{GE} = 0V, V_{CE} = V_{CES}$	-	-	1	mA	
		$V_{GE} = 0V, V_{CE} = V_{CES}, T_{case} = 125^{\circ}\text{C}$	-	-	15	mA	
I_{ces}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$	-	400	-	nA	
$V_{GE(TH)}$	Gate threshold voltage	$I_C = 20mA, V_{GE} = V_{CE}$	5.5	6.5	7.0	V	
$V_{CE(sat)}^{\dagger}$	Collector-emitter saturation voltage	$V_{GE} = 15V, I_C = 200A$	-	2.8	-	V	
		$V_{GE} = 15V, I_C = 200A, T_{case} = 125^{\circ}\text{C}$	-	3.6	-	V	
I_F	Diode forward current	DC	-	200	-	A	
I_{FM}	Diode maximum forward current	$t_p = 1ms$	-	400	-	A	
V_F^{\dagger}	Diode forward voltage	$I_F = 200A$	-	2.9	-	V	
		$I_F = 200A, T_{case} = 125^{\circ}\text{C}$	-	3.0	-	V	
C_{ies}	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$	-	36	-	nF	
C_{res}	Reverse transfer capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$	-	0.55	-	nF	
L_M	Module inductance – pins 2&3	-	-	40	-	nH	
R_{INT}	Internal transistor resistance – pins 2&3	-	-	0.5	-	$m\Omega$	
SC_{Data}	Short circuit. I_{sc}	$T_j = 125^{\circ}\text{C}, V_{cc} = 2500V,$ $t_p \leq 10\mu s, V_{ge} = \pm 15V$ $V_{CE(max)} = V_{CES} = L^* \cdot di/dt$ IEC 60747-9	I_1	-	1000	-	A
			I_2	-	930	-	A

Note:

† Measured at the auxiliary terminals

 L^* is the circuit inductance + L_M

ELECTRICAL CHARACTERISTICS
T_{case} = 25 °C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
t _{d(off)}	Turn-off delay time	I _C = 200A	-	1950	-	ns
t _f	Fall time	V _{GE} = ±15V	-	170	-	ns
E _{OFF}	Turn-off energy loss	V _{CE} = 1800V	-	220	-	mJ
t _{d(on)}	Turn-on delay time	R _{G(ON)} = R _{G(OFF)} = 16.5Ω	-	1180	-	ns
t _r	Rise time	C _{ge} = 56nF	-	225	-	ns
Q _g	Gate charge	L ~ 100nH	-	5	-	μC
E _{ON}	Turn-on energy loss	I _C = 200A, V _{GE} = ±15V, V _{CE} = 1800V, R _{G(ON)} = 7.5Ω, C _{ge} = 56nF, L ~ 100nH	-	290	-	mJ
Q _{rr}	Diode reverse recovery charge	I _F = 200A, V _R = 1800V,	-	80	-	μC
I _{rr}	Diode reverse current	di _F /dt = 1600A/μs	-	144	-	A
E _{REC}	Diode reverse recovery energy		-	75	-	mJ

T_{case} = 125 °C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
t _{d(off)}	Turn-off delay time	I _C = 200A	-	2200	-	ns
t _f	Fall time	V _{GE} = ±15V	-	190	-	ns
E _{OFF}	Turn-off energy loss	V _{CE} = 1800V	-	265	-	mJ
t _{d(on)}	Turn-on delay time	R _{G(ON)} = R _{G(OFF)} = 16.5Ω	-	1150	-	ns
t _r	Rise time	C _{ge} = 56nF, L ~ 100nH	-	280	-	ns
E _{ON}	Turn-on energy loss	I _C = 200A, V _{GE} = ±15V, V _{CE} = 1800V, R _{G(ON)} = 7.5Ω, C _{ge} = 56nF, L ~ 100nH	-	390	-	mJ
Q _{rr}	Diode reverse recovery charge	I _F = 200A, V _R = 1800V,	-	125	-	μC
I _{rr}	Diode reverse current	di _F /dt = 1600A/μs	-	155	-	A
E _{REC}	Diode reverse recovery energy		-	130	-	mJ

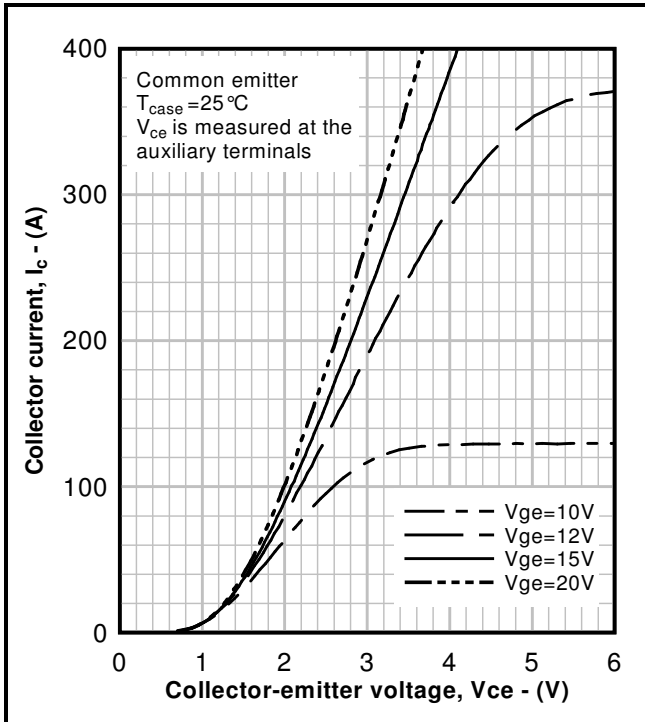


Fig.3 Typical output characteristics

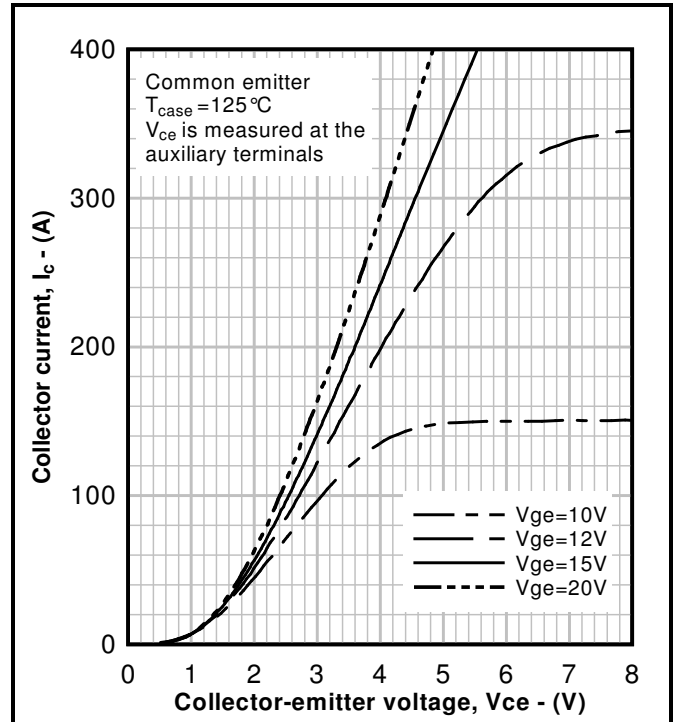


Fig.4 Typical output characteristics

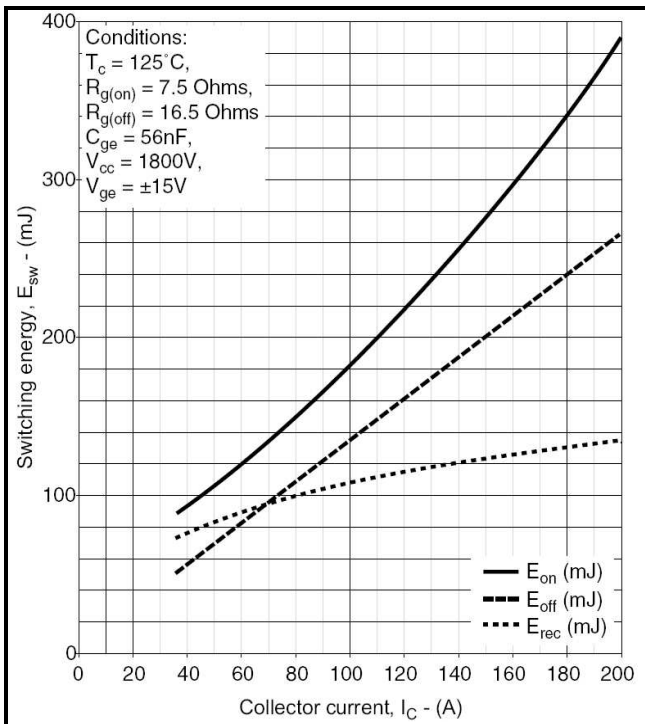


Fig.5 Typical switching energy vs collector current

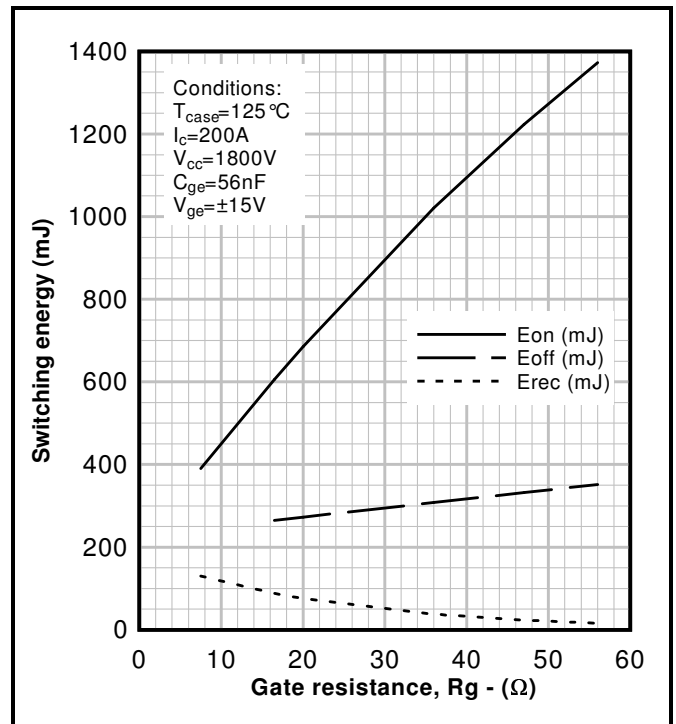


Fig.6 Typical switching energy vs gate resistance

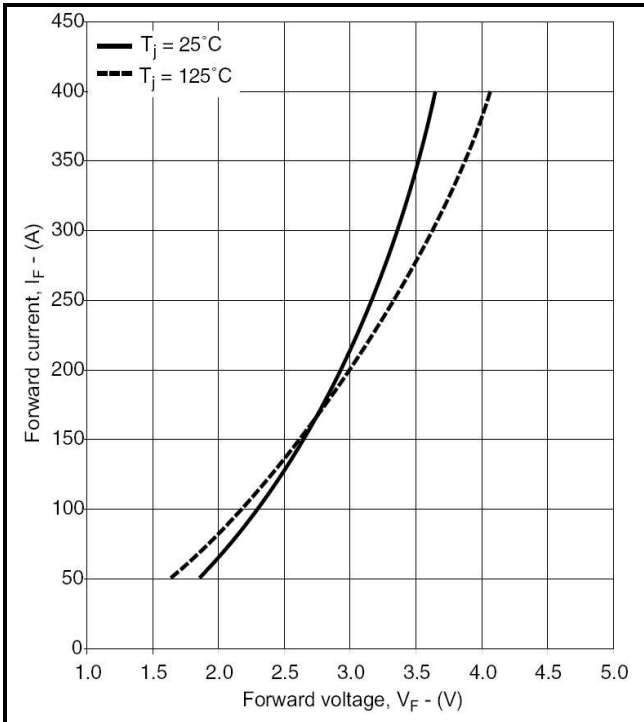


Fig.7 Diode typical forward characteristics

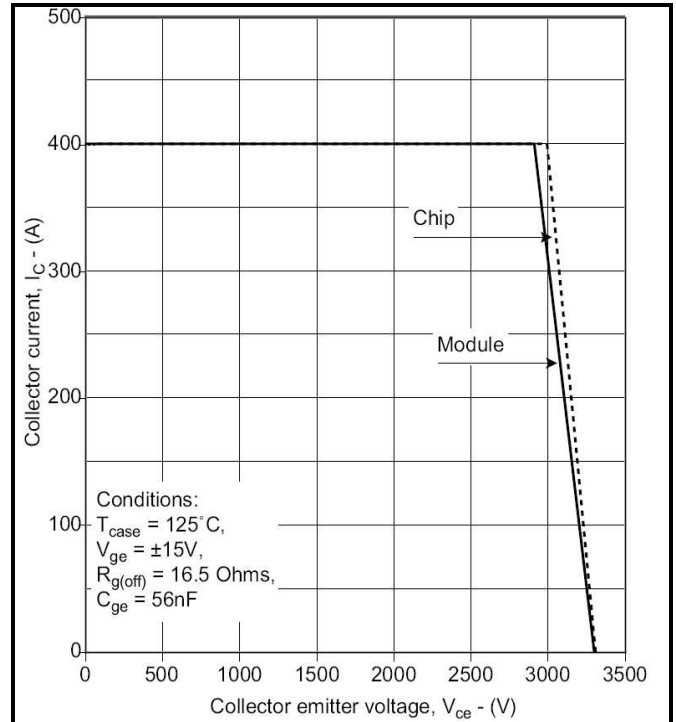


Fig.8 Reverse bias safe operating area

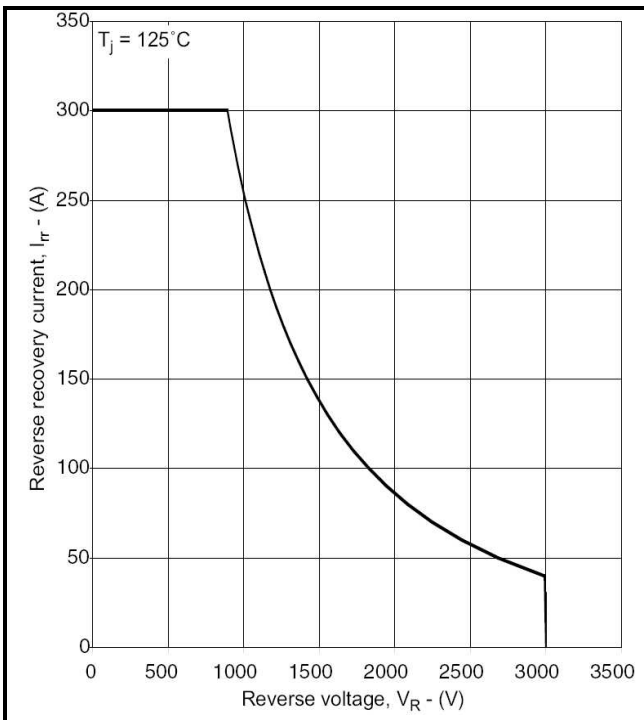


Fig.9 Diode reverse bias safe operating area

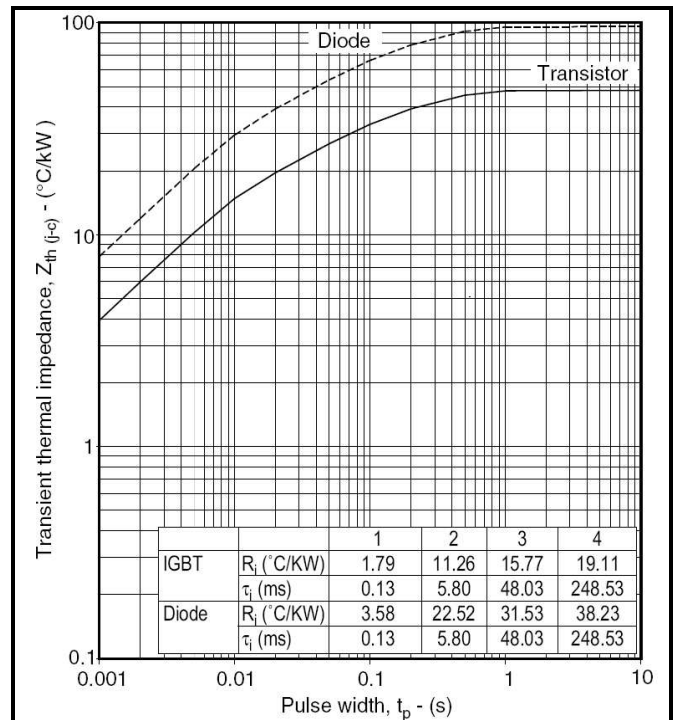


Fig.10 Transient thermal impedance

PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.
DO NOT SCALE.

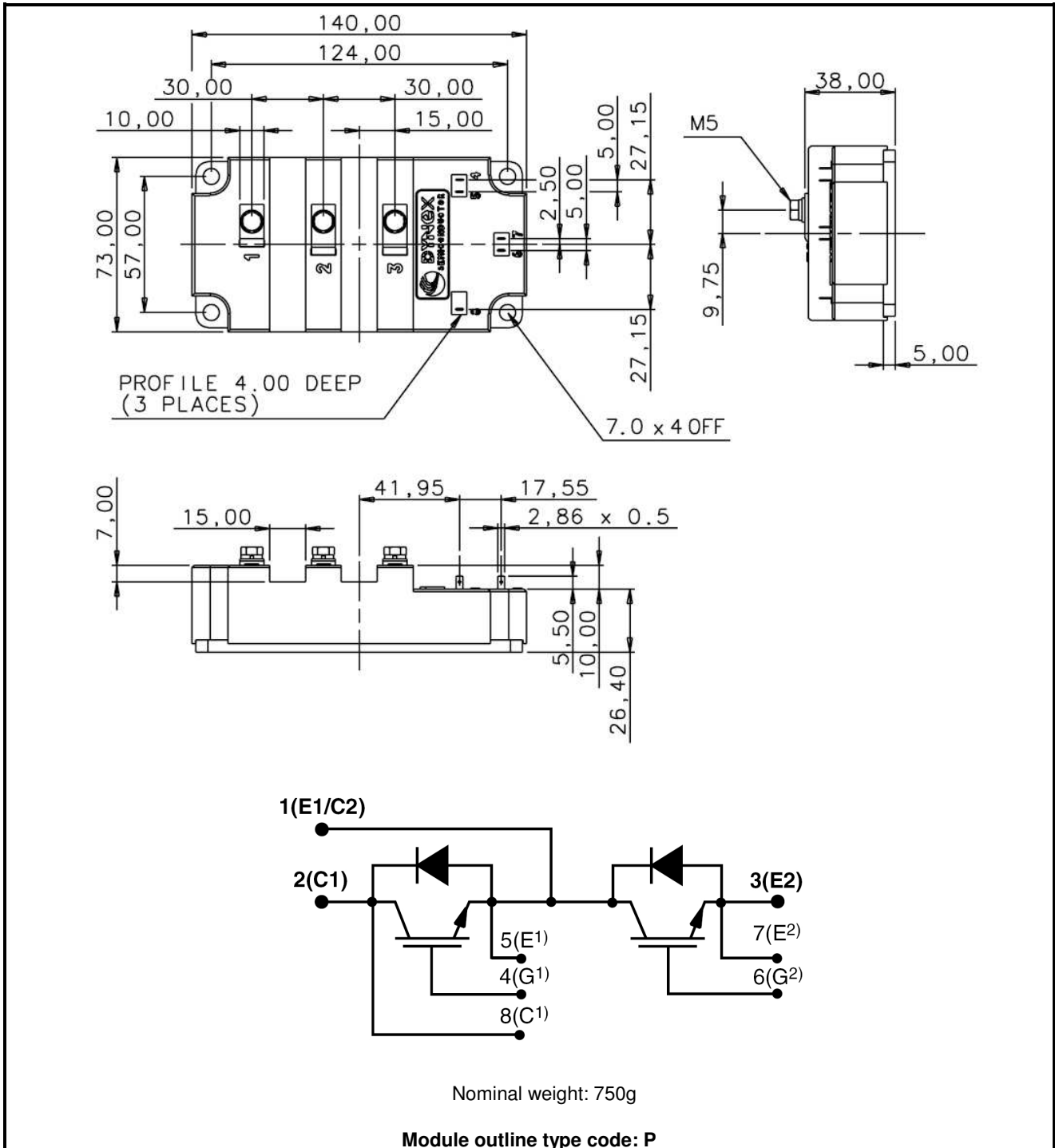


Fig. 11 Outline drawing

POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink and clamping systems in line with advances in device voltages and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group offers high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

HEATSINKS

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks which have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or Customer Services.



<http://www.dynexsemi.com>

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