

DCR1800F18

Replaces DS6067-1

DS6067-2 June 2019 (LN388

Phase Control Thyristor

(LN38848)

FEATURES

- Double Side Cooling
- High Surge Capability

KEY PARAMETERS

1800 V
1800 A
32000 A
1000 V/µs
200 A/µs

* Higher dV/dt selections available

°C, nA, DM Dutline type code: F (See Package Details for further information)

Fig. 1 Package outline

APPLICATIONS

- High Power Drives
- High Voltage Power Supplies
- Static Switches

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V _{DRM} and V _{RRM} V	Conditions
DCR1800F18 DCR1800F16 DCR1800F14 DCR1800F12	1800 1600 1400 1200	$\begin{array}{l} T_{vj} = -40^{\circ}\text{C to } 125^{\circ}\text{C}, \\ I_{DRM} = I_{RRM} = 150\text{mA}, \\ V_{DRM}, V_{RRM} t_p = 10\text{ms}, \\ V_{DSM} \& V_{RSM} = \\ V_{DRM} \& V_{RRM} + 100V \\ respectively \end{array}$

Lower voltage grades available.

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DCR1800F18

Note: Please use the complete part number when ordering and quote this number in any future correspondence relating to your order.

CURRENT RATINGS

 $T_{case} = 60^{\circ}C$ unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Si	de Cooled			
I _{T(AV)}	Mean on-state current	Half wave resistive load	1800	А
I _{T(RMS)}	RMS value	-	2820	А
Ι _Τ	Continuous (direct) on-state current	-	2550	А

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I _{TSM}	Surge (non-repetitive) on-state current	10ms half sine, $T_{case} = 125^{\circ}C$	32.0	kA
l ² t	I ² t for fusing	$V_R = 0$	5.12	MA ² s

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions	5	Min.	Max.	Units
R _{th(j-c)}	Thermal resistance – junction to case	Double side cooled	DC	-	0.02	°C/W
R _{th(c-h)}	Thermal resistance – case to heatsink	Double side cooled	DC	-	0.005	°C/W
T _{vj}	Virtual junction temperature	Blocking V _{DRM} / _{VRRM}		-	125	°C
T _{stg}	Storage temperature range			-40	140	°C
Fm	Clamping force			18	26	kN

DYNAMIC CHARACTERISTICS

Symbol	Parameter	Test Conditio	ons	Min.	Max.	Units
I _{RRM} /I _{DRM}	Peak reverse and off-state current	At V _{RRM} /V _{DRM} , T _{case} = 125°C		-	150	mA
dV/dt	Max. linear rate of rise of off-state voltage	To 67% V _{DRM} , T _j = 125°C, ga	ate open	1000	-	V/µs
dl/dt	Rate of rise of on-state current	From 67% V _{DRM} to 2000A	Repetitive 50Hz	-	200	A/µs
		Gate source 30V, 10Ω ,	Non-repetitive	-	1000	A/µs
		t _r < 0.5µs, T _j = 125°C				
V _T	On-state voltage	I _T = 2000A, T _{case} = 125°C			1.20	V
V _{T(TO)}	Threshold voltage – Low level	T _{case} = 125°C		-	0.84	V
r _T	On-state slope resistance – Low level	T _{case} = 125°C		-	0.18	mΩ
t _{gd}	Delay time	$V_D = 67\% V_{DRM}$, gate source	30V, 10Ω	-	3.0	μs
		$t_r = 0.5 \mu s, T_j = 25^{\circ}C$				
tq	Turn-off time	$T_j = 125^{\circ}C, V_R = 100V, dl/dt$	= 10A/µs,	-	250	μs
		$dV_{DR}/dt = 20V/\mu s$ linear to 67	7% V _{DRM}			
Qs	Stored charge	I _T = 2000A, tp = 1000us,T _i =	= 125°C,	-	2500	μC
I _{RR}	Reverse recovery current	dl/dt =10A/µs,		-	150	А
١L	Latching current	$T_j = 25^{\circ}C,$		-	1	А
I _H	Holding current	T _j = 25°C,		-	200	mA

GATE TRIGGER CHARACTERISTICS AND RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
V _{GT}	Gate trigger voltage	$V_{DRM} = 5V, T_{case} = 25^{\circ}C$	3	V
V_{GD}	Gate non-trigger voltage	At 40% V _{DRM} , T _{case} = 125°C	0.3	V
I _{GT}	Gate trigger current	$V_{DRM} = 5V, T_{case} = 25^{\circ}C$	300	mA
I _{GD}	Gate non-trigger current	At 40% V _{DRM,} T _{case} = 125°C	20	mA

CURVES

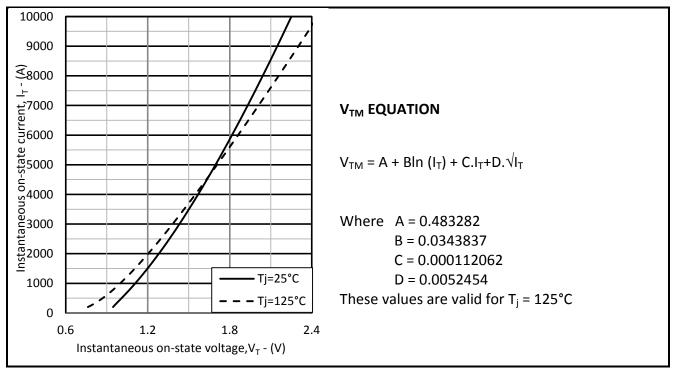


Fig.2 Maximum & minimum on-state characteristics

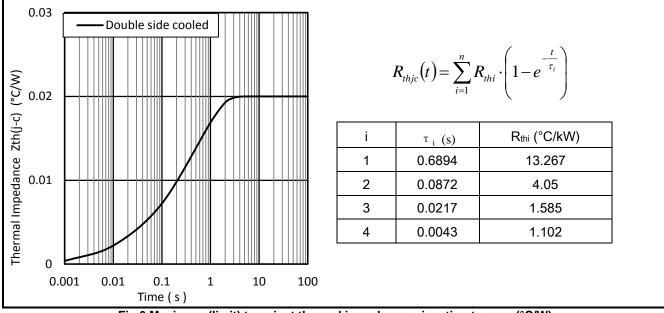


Fig.3 Maximum (limit) transient thermal impedance - junction to case (°C/W)

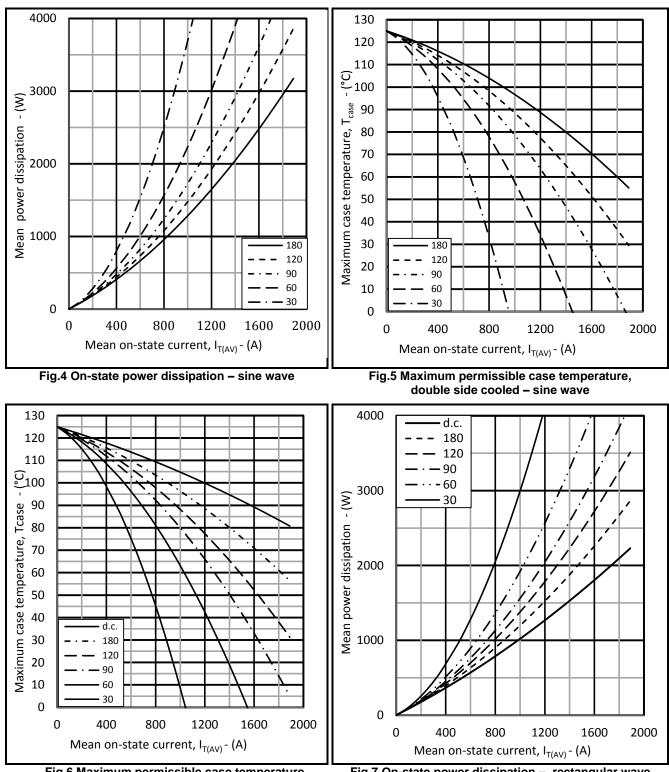


Fig.6 Maximum permissible case temperature, double side cooled - rectangular wave



DCR1800F18

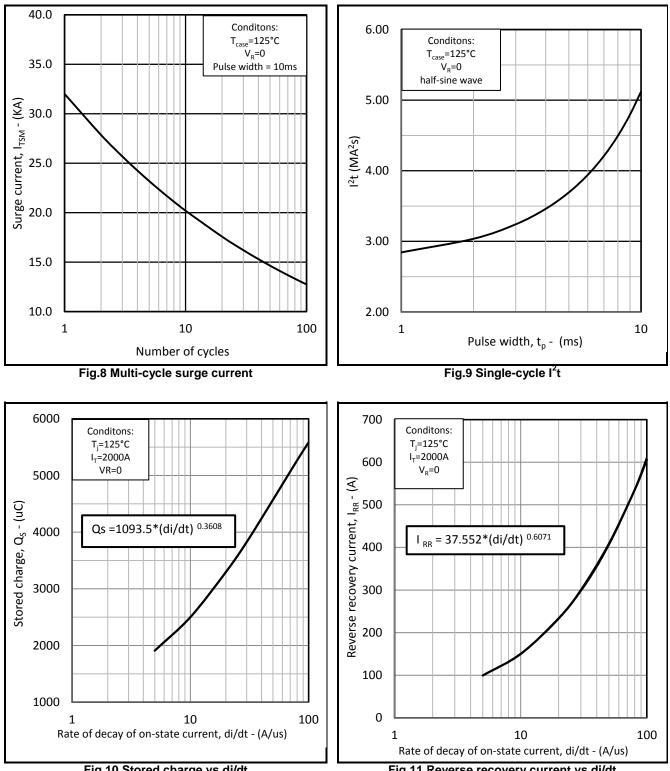
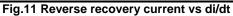


Fig.10 Stored charge vs di/dt



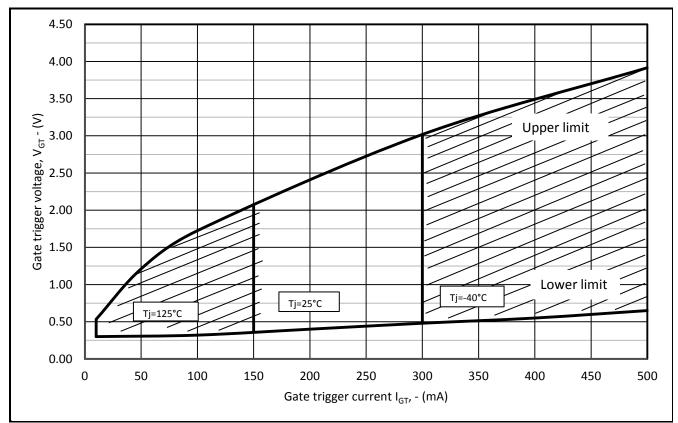


Fig.12 Gate characteristics

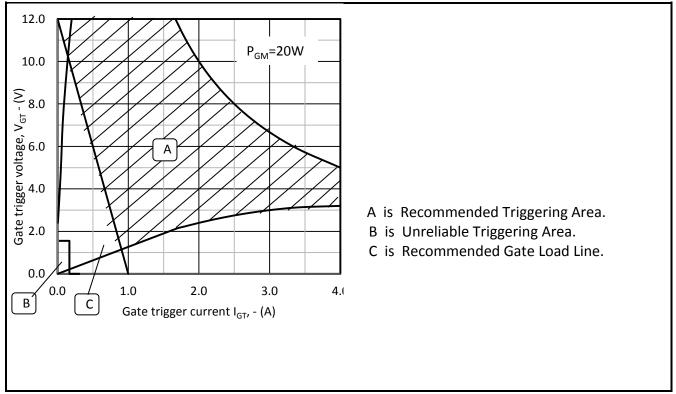


Fig.13 Gate characteristics

PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.

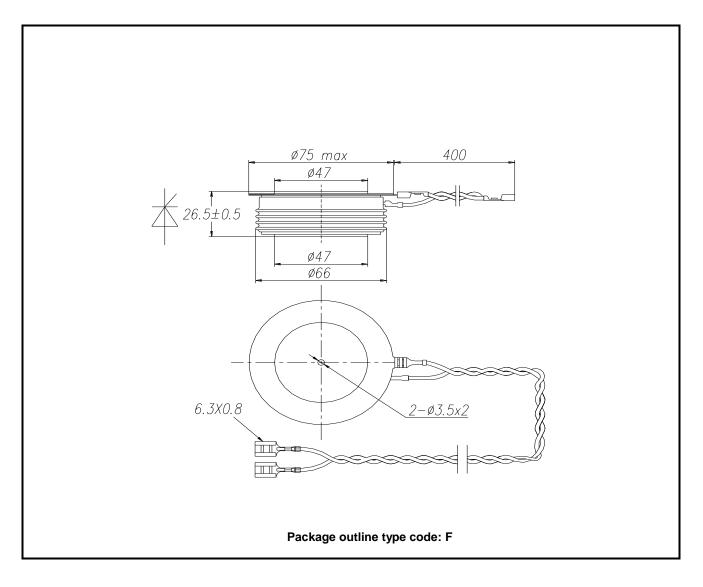


Fig.14 Package outline

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