

Model LP10A/LP25A

Logic Probes



LP10A/LP25A Specifications

TTL/CMOS Selectable	yes	yes
Signal Requirements	Levels	
Logic "1"	TTL: > 2.3 V ± 0.2 V DC CMOS: > 70 % V _{cc} ± 10%	
Logic "0"	TTL: < 0.8 V ± 0.2 V DC CMOS: < 30 % V _{cc} ± 10%	
Maximum Input Signal Frequency	20 MHz	
Minimum Detectable Pulse Width @ + 3 V		
	Frequency	Pulse Width
	< 1 kHz	100 ns
	1 kHz–20 kHz	50 ns
	20 kHz–20 MHz	30 ns
Input Impedance	1 MΩ	1 MΩ
Pulse Indicator Flash Time	500 mS	500 mS
Audible Tone at Pulse Rate	no	yes
Pulse/Memory	Pulse uses the pulse detection modes listed above. Memory allows the probe to capture a positive or negative pulse greater than 30 nS in width. The yellow LED will come on and stay ON until switch is reset to PULSE.	
Power Requirements (from circuit under test)	LP10A	LP25A
Power Supply	5 - 15 V DC	5 - 15 V DC
Power Supply Current @ 5 V	30 mA	40 mA
@ 15 V	80 mA	90 mA
Power Supply Protection	20 V AC/V DC	20 V AC/V DC
Maximum Input Voltage (15 seconds)	40 V AC/V DC	40 V AC/V DC

General Specifications

Indoor Use; Operating Temperature: 0 °C to 50 °C at 80 % R.H.; Storage Temperature: - 20 °C to 65 °C at 75 % R.H.; Dimensions: 210 mm x 18 mm x 18 mm (8.2" x 0.7" x 0.7"); Weight: 38.1 g (1.3 oz.) 42.6 g (1.5 oz.)

- Pen-sized logic probes
- Troubleshoot and analyze digital logic circuits



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