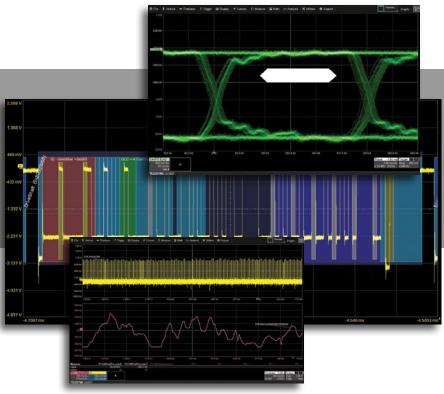


Low-speed Serial Data Trigger, Decode, Measure/Graph, and Eye Diagrams



Key Features

More than 20 supported standards:

- I²C
- SPI, SPI DDR, Simplified SPI
- UART and RS-232
- USB 1.x, 2.0 and USB 2.0 HSIC
- CAN, CAN FD, and J1939
- LIN
- FlexRay
- SENT
- ARINC 429
- MIL-STD-1553
- I³C
- SPMI
- SPACEWIRE
- 10/100 Base-T Ethernet, EtherCAT
- DPHY
- DigRF 3G, DigRF v4
- I²S, incl. LJ, RJ, TDM
- Manchester
- NRZ

Most powerful, flexible triggering capabilities

Intuitive, color-coded decode overlays

Single protocol results table supports up to 4 decoders at one time

Unique measure/graph capabilities:

- Automated timing measurements
- Serial DAC extract digital data and plot it as a waveform
- Bus parameters

Physical layer eye diagrams

Teledyne LeCroy's Trigger (T), Decode (D), Measure/Graph (M or G) and Eye Diagram and Physical Layer (E or P) options are the best in the industry and nearly universally available across the entire Teledyne LeCroy oscilloscope product line.

Highest Performance Triggers

Designed by people who know the standards, with the unique capabilities you want to isolate unusual events. Conditional data triggering permits maximum flexibility, and highly adaptable error frame triggering is available to isolate error conditions. Frame definition allows grouping of UART or SPI packets into message frames for customization.

The Best Serial Decoder

Decoded protocol information is colorcoded to specific portions of the serial data waveform and transparently overlaid for an intuitive, easy-tounderstand visual record. All decoded protocols are displayed in a single time-interleaved table. Touch a row in the interactive table to quickly zoom to a packet of interest and easily search through long records for specific protocol events using the built-in search feature.

Measure/Graph Tools for Validation Efficiency

Quickly validate cause and effect with automated timing measurements to or from an analog signal or another serial message. Make multiple measurements in a single long acquisition to quickly acquire statistics during corner-case testing. Serial (digital) data can be extracted to an analog value and graphed to monitor system performance over time, as if it was probed directly. Complete validation faster and gain better insight.

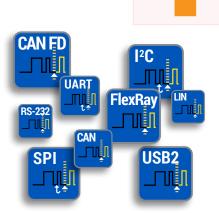
Eye Diagrams & Physical Layer

Rapidly display an eye diagram of your packetized low-speed serial data signal without additional setup time. Use eye parameters to quantify system performance and apply a standard or custom mask to identify anomalies. Mask failures can be indicated and can force the scope into Stop mode.

HIGHEST PERFORMANCE TRIGGERS

Every serial trigger we design exhibits deep knowledge of the standard. Most serial triggers work with digital (MSO) inputs, or the EXT input for the Clock line so as to conserve analog channels. Each serial trigger has some unique aspect for high performance, such as:

- I²C trigger permits triggering on data in a specific location of an up to 2048 byte I²C EEPROM read or write.
- UART or SPI bytes can be combined into a single "message frame" trigger on custom protocols based on UART or SPI byte blocks.
- UART supports 9-bit "address" or "wakeup" mode triggering.
- CAN, CAN FD, LIN, FlexRay and MIL-STD-1553 permit conditional ID/Address triggering.
- CAN and CAN FD permit triggering symbolically using a DBC file.
- USB 2.0 and MIL-STD-1553 triggers permits complex transaction definition and triggering.



High performance and a wide range of support for serial data standards

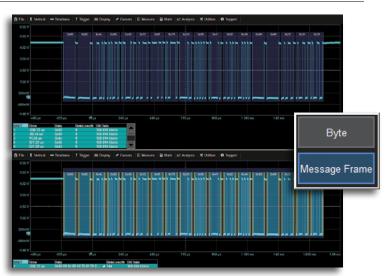
Trigger I2C Trigg	gerScan			🙁 CLOS
Source Setup SDA (DATA) C1 SCL (CLK) C2 Threshold	Trigger Type Start Stop No Ack Addr Prame Length EE PROM	Setup Format Address Setup Binary 7 Bits 10 Bits Hex Address	Data Pattern Setup Include RW Bit Superstant Direction Write Q2 Equal Value Equal Equal End Less than	Ack Setup No Ack
	gering selections provide hig	hest flexibility	Image: Second secon	

Conditional DATA Setup

Every Teledyne LeCroy low-speed serial trigger that incorporates DATA trigger permits a conditional (<. <=, =, >, >=, <>, inside a range, outside a range) setup for the DATA condition. This is especially useful in situations where abnormal events should be monitored, such as when a temperature sensor transmitting via I²C exceeds a maximum temperature, or a CAN node broadcasts a low or high engine RPM or coolant pressure. Furthermore, data for triggering can be specifically isolated in very long byte streams to specific bit locations, even those which span data bytes.

Support for Many Proprietary Protocols

Many proprietary serial protocols make use of the common UART (single Data line) or USART (Clock and Data lines, e.g., SPI) byte structures, with multiple bytes grouped into proprietary protocol definitions. Our highly flexible UART byte and SPI format definitions accommodate nearly any customer need, and the UART or SPI bytes can be defined to be part of a single "message frame" through use of our Interframe Setup. Then, the trigger pattern setup can isolate any byte value, e.g., an ID, or a DATA string value, that is part of your proprietary protocol message definition.



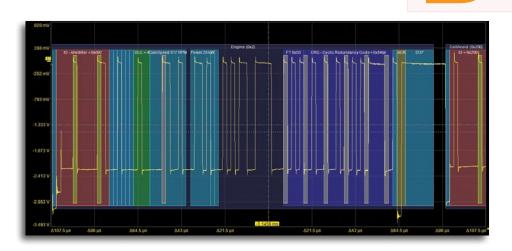
Byte mode (top) treats each byte uniquely. Message Frame mode (bottom) groups bytes into on single, long multi-byte message

SIMPLY THE BEST SERIAL DECODER

Our serial decode, search and table tools work exactly the way you want. These tools are the industry standard for turning your oscilloscope into a protocol analyzer with fast and intuitive correlation of protocol data to the physical layer waveforms.

Intuitive, Color-Coded Overlays

A transparent overlay with color-coding for specific portions of each protocol and the entire message frame makes it easy to understand your serial data information. Unlike other solutions, with protocol decode information away from the signal, our solution correlates the waveform and the protocol decode directly on the display. As the acquisition length is expanded or shortened, the decode overlay will adjust to show you just the right amount of information.





Pattern Search

All decoders provide ability to search through a long record of decoded data by using a variety of search criteria, or values, or simply finding the next occurrence. Pattern Search automatically creates a zoom trace of the acquired waveform and displays the selected location complete with the transparent color-coded overlay.

Index	Time	Protocol	Message	Data	CRC	St.	atus	
▶ 91	-154.63 ms	SIOP		0x00				
▶ 92	-154.54 ms	SIOP		0x31				
▶ 93	-154.44 ms	SIOP		0x36				
▶ 94	-154.34 ms	SIOP		0x34				
▶ 95	-154.32 ms	UART				BF	REAK	
▶ 96	-146.75 ms	CAN Std	Std 0x400	6a 6b	0x3c	:c7		
Þ 97	-144.96 ms	CAN Std	Std 0x200	21	0x44	169		
▶ <mark>98</mark>	-144.87 ms	CAN Std	Std 0x210	00	0x98	33		
4 99	-137.15 ms	CAN Std	Std 0x410	70 71 72 73 74 75 76 77	0x5e	95		
		Format	ID IDE RTR D	LC Data C	RC Bi	tRate S	Status	
		Std	0x410 0 0 8	70 71 72 73 74 75 76 77 07	<5e95 0.0	0 nb/s		
▶ 100	-136.72 ms	CAN Std	Std 0x400	6a 6b	0x3c			
li 101	-134.89 ms	CAN Std	Std 0x200	3f	0xb9			
Þ 102	-134.80 ms	CAN Std	Std 0x210	00	0x98	3		
Þ 103	-132.94 ms	UART		0x00 00 4c 65 43 72 6f 79 20 55 41 52 54 00 00 32 30 38				
▶ 104	-132.18 ms	CAN Std	Std Ext 0x18ccdd11	80 81	0x1c			
▶ 105	-130.88 ms	UART					REAK	
▶ 106	-129.82 ms	CAN Std	Std Ext 0x18aabb01	55 aa	0x36			
Þ 107	-129.70 ms	CAN Std	Std Ext 0x18aabb02	55 aa ff	0x36	615		
108	-126.69 ms	CAN Std	Std 0x400	6a 6b	0x3c	cc7		

Interactive Table Summarizes Results

Turn the oscilloscope into a protocol analyzer with a tabular display of decoded information. Customize the table to show only the data of interest and touch a message in the table to automatically zoom to it and display it on the screen. Export the table for offline analysis. Up to four different decoded signals of any type may be simultaneously displayed in the table.

MEASUREMENT / GRAPHING - EXTEND YOUR KNOWLEDGE

Key Features

Timing measurements

- Serial Message to Analog Signal
- Analog Signal to Serial Message
- Serial Message to Serial Message

Serial DAC measurement/graphing

Bus status measurements

Automated – quickly gather statistics, display Histograms

Quickly correlate cause-effect timing relationships to other events

Conditional filtering

Supported for

- |²C
- SPI, SPI DDR, Simplified SPI, Custom SPI
- UART
- RS-232
- USB 1.x and 2.0
- CAN
- CAN FD
- LIN
- FlexRay
- ARINC 429
- MIL-STD-1553
- I³C
- SPMI
- DigRF 3G
- DigRF v4
- I²S, incl. LJ, RJ, TDM (note: Serial DAC graph, or "G" capability only)

The measurement and graphing capabilities significantly enhance our trigger and decode packages, and help you debug and validate faster.

Digital data can be extracted and rescaled to an analog value and graphed over time, time-correlated to other acquired data, as if you had probed it directly. It's a Serial Data DAC!

Automated cause-effect timing measurements can be made between analog signals and serial data messages, or two serial data messages. Use with serial triggering and long acquisitions to understand system behavior during stress or corner-case testing. A variety of bus status measurements are also available.

All measurements may be used with the rich set of standard Teledyne LeCroy standard parameter analysis tools, including automated pass/fail analysis with boolean test conditions, measurement gates, measurement accept, filtering, parameter math, and custom math.



Setup is easy in the Measure/Graph setup tab:

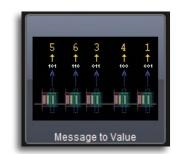
- 1. Choose the source
- 2. Choose the measurement
- 3. Select the destination parameter (e.g., P2)
- 4. Then apply and configure

Serial Data DAC and Graphing Tools

Digital data can be extracted from specific locations in the serial data message using the Message to Value measurement parameter - a serial data DAC. This information can then be displayed as a measurement parameter value(s), or it can be viewed as a time-correlated waveform displaying the measurement value over time - as if you were able to probe and acquire it directly. Use the long acquisition time of the oscilloscope to understand how the data changes over long periods of time, in conjunction with other system behaviors.

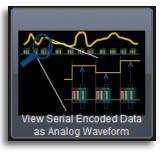
Some examples of the usefulness of this capability are:

- Viewing I²C or SPI temperature sensor data
- Viewing DigRF 3G radio frequency I and Q modulated signals
- Viewing CAN wheel speed information used by an ABS
- Viewing reconstructed analog audio from serial I²S streams

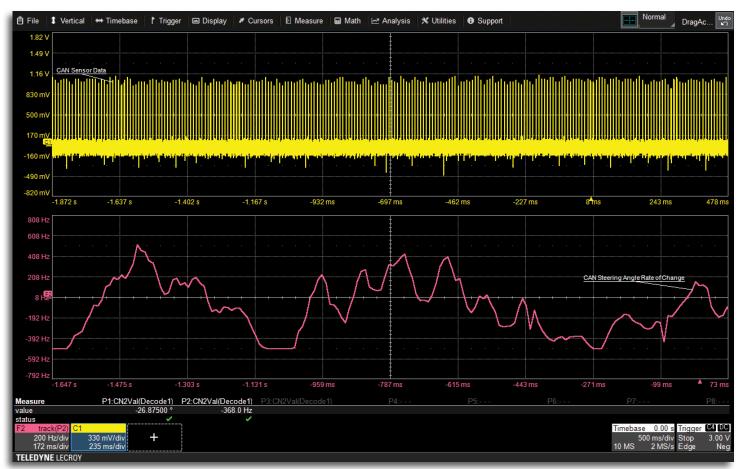




Decoded data content of data payload of a protocol message meeting conditions.



Applies a Track math operator to the Message to Value measurement to view Serial Encoded Data as an Analog Waveform



Shown above is a long acquisition of a CAN serial data signal (top waveform) that contains embedded digital data for steering wheel angle rate of change (deg/s, or Hz). The Message to Value parameter was configured to locate and extract the digital steering wheel angle range data from particular locations in specific CAN serial messages, and then converted from digital to analog form with proper re-scaling and physical units. The serial data DAC waveform (bottom waveform) is shown in the lower grid.

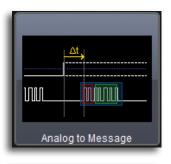
MEASUREMENT / GRAPHING - IMPROVE VALIDATION TIME

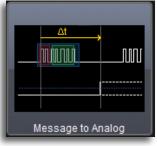
Automated Timing Measurements

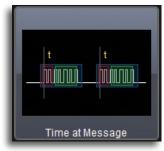
Utilize a serial trigger to isolate a specific message and then measure a cause-effect timing relationship with a subsequent analog signal, or vice versa. But instead of manually measuring the timing with cursors, use these tools to automate the measurement and return thousands of values quickly as your system undergoes stress testing. Automate the measurement and validation of gateway latency times from one serial message to another (e.g. CAN to LIN or low-speed CAN to high-



speed CAN, or CAN to FlexRay) without having to manually use cursors or compare values and times in a protocol table. Quickly understand bus latency times or arbitration behaviors by measuring the difference between two messages on a single decoded waveform. Dramatically improve your validation efficiency and time to insight







Analog to Message

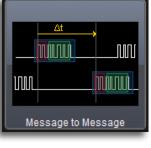
Computes the time difference from a protocol message meeting specified conditions to the crossing of a threshold on an analog signal.

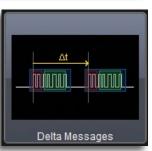
Message to Analog

Computes the time difference from a protocol message meeting specified conditions to the crossing of a threshold on an analog signal.

Time@Message

Time from Trigger to each protocol message meeting specified conditions.





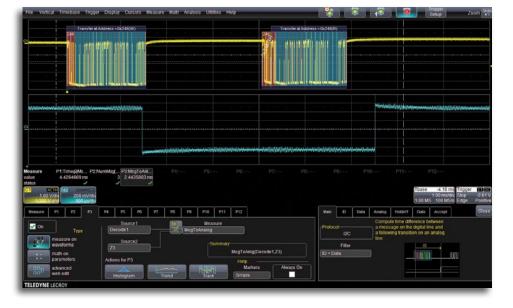
Message to Message

Computes the time difference from a protocol message meeting specified conditions to another protocol message meeting specified conditions

DeltaMessage Time

Computes the time difference between two messages on a single decoded line.

Use the Message to Analog measurement to find the time between an I²C data packet and a control signal on another channel. Multiple measurements in one or more triggers could be made to understand behaviors over time or under different operation conditions



7

Bus Status Measurements

Bus Load

Message Bus Load %

on the bus

Computes the load of user

defined message in percent

The bus status measurements Bus Load, Message Bitrate, and Number of Messages, give an overall status of the decode protocol to quickly learn if the bus is over utilized and to verify the bit rate matches expectations.

The Perfect Oscilloscopes for the TDME Options

Teledyne LeCroy HDO, WavePro HD, and WaveRunner oscilloscopes are the perfect oscilloscope platforms to utilize the TD and TDME toolsets.

Teledyne LeCroy's 12-bit High Definition Oscilloscopes (HDOs), such as WaveRunner 8000HD, WavePro HD, HD06000A, and WaveSurfer 4000HD provide 12-bit resolution and either 4 or 8 analog input channels up to 8 GHz with MSO digital input options. These oscilloscopes have powerful standard toolsets for debugging deeply embedded designs with analog, digital, serial data, and sensor signals. Their 12-bit resolution is ideal for measuring sensor signals and correlating them to other system activities. 8 analog input channels provides more ability to correlate more signals to each other.

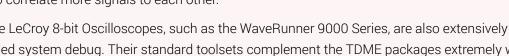
Teledyne LeCroy 8-bit Oscilloscopes, such as the WaveRunner 9000 Series, are also extensively used for embedded system debug. Their standard toolsets complement the TDME packages extremely well.

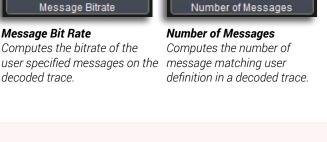
Message Bitrate

Computes the bitrate of the

Message Bit Rate

decoded trace.





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EYE DIAGRAM AND PHYSICAL LAYER

Eye Diagrams are "bit-sliced" views of the physical layer serial data waveforms. They provide a fast, intuitive way to understand physical layer signal integrity. Eye Diagrams may be combined with masks and mask failure indications, and eye (opening) parameters. Protocols with challenging topologies (e.g. FlexRay) provide even more advanced measurement capabilities.



- Up to four simultaneous Eye Diagrams
- Simple to set up one button push

Include standard or custom masks. Create your own masks.

Eye parameters

Mask failure indication

Failure locator trace waveform

Pass/Fail with STOP on failure

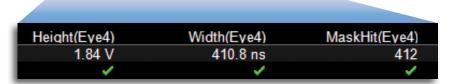


Eye diagrams "slice" each bit and overlay them to get a consolidated view of signal quality. Intrusions into the eye opening or onto a mask indicate potential problems.



Up to 4 Simultaneous Eye Diagrams

Up to four serial data signals can be decoded and displayed as eye diagrams at one time. These can be different protocols, or the same protocol measured at different points (e.g., transmit and receive, different nodes, or different standard-defined test points). Apply a user-defined filter to each eye diagram to only display specific signals in the eye.



Eye Diagram Measurement Parameters

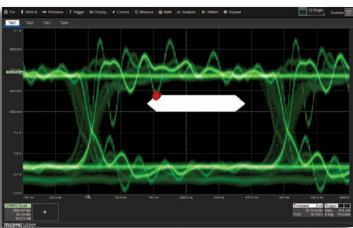
Quantify physical layer signal quality in the eye by applying parameters for Eye Height, Eye Width, and Number of Mask Failures. Some packages (e.g. FlexRay TDMP) go a step further and include additional measurements defined in the standard.

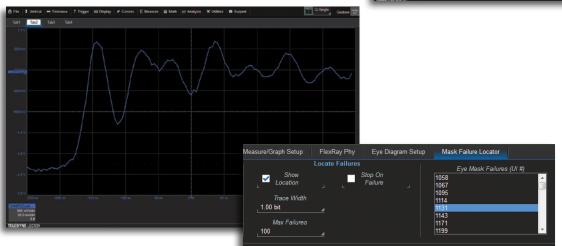
MASK AND MASK FAILURE INDICATION

Ε

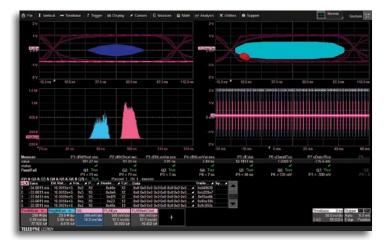
Mask and Mask Failure Indication

A user-defined or pre-defined mask may be added to the eye diagram so as to objectively evaluate if the physical layer signal intrudes too far into the eye opening. Apply a filter to include or exclude specific messages from the Eye so as to determine failure source (e.g., messages from a specific node or with a specific ID). Mask failures are indicated with a red circle and can be displayed in a table. Touch the failure table to open a zoom of the failed area for further inspection





PHYSICAL LAYER (EYE + ADVANCED MEASUREMENTS)



Some standards, due to their speed or nodal complexity, provide specific guidance on what eye diagrams or measurements should be made and exactly how they should be performed. FlexRay, and MIPI DPHY are examples. In these cases, the Eye Diagram ("E") capability is augmented with additional specialized "P" capability (for Physical Layer Measurements), per the standard. In these cases, the "E" capabilities previously described are also available.

Key Features

Set an ACK condition (ACK, NO ACK, Don't Care) in all frame trigger setups

Does not require clock trace to be displayed during decode

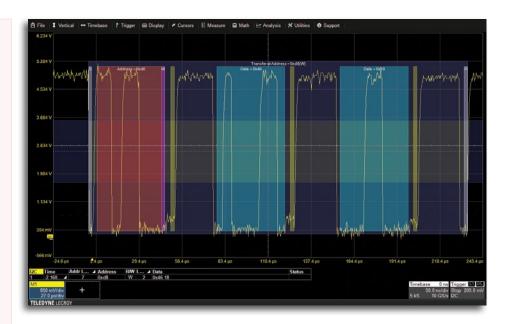
EEPROM read/write 2048 byte trigger capability

Frame Length trigger capability

Address can include a R/W bit, or define as Don't Care

Use analog or digital (MSO) inputs for acquisition and triggering.

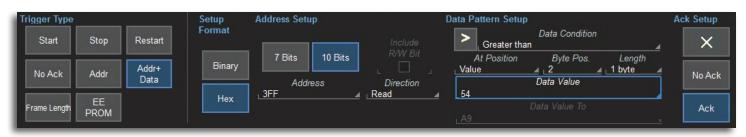
EXT input may be used for clock signal



Trigger Type			Setup	Address Setup	()ata Pattern Setup	Silua da se da		1	Ack Setup
Start	Stop	Restart	Format			·· 🛏 · In Range of	ata Condition			×
		Addr+	Binary	7 Bits 10 Bits	R/W Bit	At Position	Byte Pos.	Length 3 bytes		
No Ack	Addr	Data		Address		Da	ata Value			No Ack
Frame Length	EE PROM		Hex	LAX.		_ 465BA1 	ta Value To			Ack

More Trigger Choices

In addition to typical Start/Stop/ReStart, NoAck, Address and Address+Data triggers, Teledyne LeCroy provides triggering for EEPROM read/writes up to 2048 bytes long and for Frame Length. Address-based triggers permit an additional ACK condition (ACK present, NO ACK present, or DON'T CARE). and selection to include a R/W bit in a 7-bit trigger.



More Flexibility for Address-based Triggers

Address-based triggers permit an additional ACK condition (ACK present, NO ACK present, or DON'T CARE). and selection to define the transfer direction as a READ, WRITE or DON'T CARE (using R/W bit in a 7-bit trigger, or R/W Direction selection in a 10-bit trigger).

	I2Cbus TD and I2Cbus TDME
Definition	
Source Setup	Select Source for Clock and Data.
Trigger Capability	
Format	Hexadecimal or Binary. ADDRESS and DATA can be set up with different formats.
Trigger Setup	Trigger on START, ReSTART, STOP, Missing ACK, ADDR, DATA, ADDR+DATA, ADDR+DATA FRAME LENGTH, EEPROM DATA TRANSFER
ADDRESS Setup	Specify one ADDRESS with condition of "=". 7 or 10 bit ADDRESS supported with full Read, Write, or R/W="Don't Care" selectability on both 7 and 10 bit ADDRESSes. Choose to Trigger on address values that include/don't include R/W bit in address value.
DATA Setup	ADDRESS+DATA Trigger Type: Hexadecimal: # Data Bytes = 0 to 12. Data can be defined by nibble. Binary: Any combination of 0,1, or X for 1-96 bits Data pattern can be set to start at any location in the 12 Byte / 96 bit sequence. ADDRESS+ DATA FRAME LENGTH Trigger Type: # Data Bytes = 0 to 2047. EEPROM DTA TRANSFER Trigger Type: # Data Bytes = 0 to 12. Data can be defined by nibble. Data pattern can be set to start at the beginning of any byte in an up to 2048 byte sequence.
DATA Conditions	<=, <, =, >, >=, <>, IN RANGE, OUT OF RANGE, or DON'T CARE.
ACK Conditions	For any ADDR, ADDR+DATA, ADDR+DATA FRAME LENGTH, or EEPROM DATA TRANSFER setup, select an ACK Condition of ACK, NO ACK, and DON'T CARE.
Bit Rates	Full range over I2C specification for Standard, Fast, Fast-Mode Plus, and High-Speed modes. Auto-detected.
Trigger Input	Any analog Channel or Digital input, or the EXT input. Clock may be input to EXT to conserve available analog Channels.
Decode + Search (Capability
Format	Hexadecimal, Binary, ASCII.
Decode Setup	Threshold definition required. Default is to Percent amplitude. Choose to Decode address values including/not including the R/W bit in address value.
Decode Input	Any analog Channel, Memory or Math trace, and any Digital trace. Clock channel may be turned OFF and data will still decode (reduces screen clutter)
# of Decodes	Up to 4 buses may be decoded at one time. In addition, zooms can be displayed (with decoded information).
Visual Aid	Color Coding for FRAME, START/ReSTART bit, ADDR, R/W, DATA, ACK, NACK, and STOP bit. Decode information is intelligently annotated based on timebase setting, and overlaid on acquired waveform.
Table Configure, Export Table	Display 1 to 20 rows of decoded information for up to 4 different protocols or decodes in time order in a single table. Displayed information includes Index, Timestamp, and other various protocol-specific information. Table permits scrolling, touch to zoom, export to .csv file, and special display of long data or other patterns.
Pattern Search	Search for Previous or Next ADDRESS, PACKET, or DATA in hexadecimal format.

I2Cbus TDME only

Measure / Graph C	apability	
Serial Data Digital-to-Analog Conversion (DAC)	Message to Value measurement parameter extracts and converts a specified portion of the data in an up to 2048 byte data/payload in the serial message and displays it as an analog decimal value. Supports different data encoding formats, message filtering to specific IDs, and complete re-scaling with unit conversion. Serial DAC Waveform plots the converted digital-to-analog data as a waveform time-correlated to other acquisition data, and view the change in data over time.	
Timing Measurements	Message to Analog, Analog to Message, Message to Message, ΔMessage Time (identical message on same decoder), Time@Message (time from trigger) measurement parameters. Serial Message may be defined by "ID =" (where applicable) and user-defined DATA condition of <=, <, =, >, >=, <>, IN RANGE, or OUT OF RANGE in any location in up to 2048 bits of data. Analog Signal may be defined by Slope (pos, neg), Level (abs or %) with Hysteresis setting. Holdoff may be set on the Analog Signal by either Time or Events (up to 1000) to preclude unwanted measurements.	
Bus Status Measurements	Number of Messages, Message Bit Rate, Message Bus Load %: Serial Message may be defined by "ID =" (where applicable) and user-defined "DATA <=, <, =, >, >=, <>, in range, out of range" in any location in up to 2048 bits of data.	
Eye Diagram Capal	bility	
Setup	Create up to four simultaneous Eye Diagrams (one per Serial Decoder) of the physical layer signal(s). Eye Style selectable as color- or analog-persisted. Eye Saturation adjustable from 0 to 100%.	
Eye Parameters	Eye Height, Eye Width, (Number of) Mask Hits	
Eye Mask	Create a custom Mask using the free Teledyne LeCroy MaskMaker software utility. Store custom masks for later recall and use.	
Failure Indication and Location	Mask Failure Indication ON or OFF (ON = indicated with a red circle). Mask Failure Location trace waveform displayed and interactive with Eye Mask failure table. Supports STOP trigger on Mask Failure.	

Key Features

Supports nearly any type of SPI, including simplified SPI with no Chip Select and SPI DDR

Interframe message time setup permits Frame definition for support of many proprietary USART-based protocols

Flexible Bits/Word Decode Setup

Does not require clock trace to be displayed during decode

Use analog or digital (MSO) inputs for acquisition and triggering.

EXT input may be used for clock signal



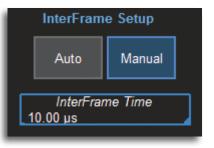


SPI Triggering When No Chip Select is Present

Most SPI triggers require that a Chip Select signal be present. However, simplified (single-master, single-slave) SPI (also referred to as SSPI or SIOP) has no Chip Select. Our solution still permits triggering on simplified SPI through use of an Interframe Setup time. In most cases, the AUTO default provides accurate results, but MANUAL selection is also available.

Interframe Message Time Setup for Proprietary USART-based Protocols

Two line (CLOCK and DATA) serial data signals with proprietary formats are sometimes used. This is commonly known as a USART. Given the flexibility of the setup of our trigger and decoder, it is often possible to use the Interframe Message Time Setup to "packetize" consecutive bytes into one Message Frame, and then trigger on serial data in a particular byte location in the complete multi-byte Frame. Then, SPI-CUSTOM may be used to decode information as a complete Frame instead of individual Bytes.



	SPIbus TD and SPIbus TDME
Definition	
Source and Protocol Setup	Select Source for Clock, Data, and Chip/Slave Select (Chip/Slave Select not required for SIOP or SSPI types). Select SPI Type (SPI, SIOP, SSPI, or SPI-DDR). SPI Type CUSTOM is also available in the decoder. For SPI or SPI-Custom, select CPOL (Clock Polarity 0 or 1) and CPHA (DATA Polarity 0 or 1) (SIOP permits CPOL selection of 0 or 1, but CPHA =1; SSPI CPOL=1, CPHA=1; SPI-DDR does not have CPOL or CPHA selection). Select DATA = MSB or LSB. Select InterFrame Setup to Auto or Manual.
Trigger Capability	
Format	Hexadecimal or Binary
Trigger Setup	Trigger on DATA for any of the five SPI Modes with either MSB/LSB and with or without Slave Select. InterFrame Setup permits user-definition of expected maximum time between clock bits so as to permit triggering with CLOCK and DATA signals in the absence of a CHIP/SLAVE SELECT signal (SIOP or SSPI). Typically, the InterFrame AUTO default of 4x a bit length is sufficient, but this can be set to any value.
DATA Setup	Hexadecimal: # Data Bytes = 0 to 12. Data can be defined by nibble. Binary: Any combination of 0,1, or X for 1-96 bits
	Data pattern can be set to start at any location in the 12 Byte / 96 bit sequence.
	InterFrame Time Setup is available for SIOP, SSPI, and SPI-CUSTOM Types. This permits user-definition of expected maximum time between clock bits so as to permit decoding in the absence of a Chip/Slave Select signal. The InterFrame AUTO default of 4x a bit length is typically sufficient, but this can be set to any value up to 10 seconds. It also enables definition of multiple SPI (any Type) byte packets into a single long message package of multiple SPI Data bytes, over which up to 12 Data Bytes can be defined for triggering (as described above).
DATA Condition Setup	<=, <, =, >, >=, <>, IN RANGE, OUT OF RANGE, or DON'T CARE
Bit Rates	Any, up to 25 Mb/s (typical). Auto-detected from clock signal.
Trigger Input	Any analog Channel or Digital input, or the EXT input. Clock or Chip/Slave Select may be input to EXT to conserve available analog Channels.
Decode + Search	Capability
Format	Hexadecimal, Binary, ASCII
Decode Setup	Threshold definition required. Default is to Percent amplitude. Choose to Decode address values including/not including the R/W bit in address value.
Decode Input	Any analog Channel, Memory or Math trace, and any Digital trace. Clock channel may be turned OFF and data will still decode (reduces screen clutter)
# of Decoded Waveforms	Up to 4 buses may be decoded at one time. In addition, zooms can be displayed (with decoded information).
Visual Aid	Color Coding for FRAME, START/ReSTART bit, ADDR, R/W, DATA, ACK, NACK, and STOP bit. Decode information is intelligently annotated based on timebase setting.
Table Configure, Export Table	Display 1 to 20 rows of decoded information for up to 4 different protocols or decodes in time order in a single table. Displayed information includes Index, Timestamp, and other various protocol-specific information. Table permits scrolling, touch to zoom, export to .csv file, and special display of long data or other patterns.
Pattern Search	Search for Previous or Next MESSAGE or DATA Pattern in hexadecimal format.

SPIbus TDME only

apability
Message to Value measurement parameter extracts and converts a specified portion of the data in an up to 2048 byte data/payload in
the serial message and displays it as an analog decimal value. Supports different data encoding formats, message filtering to specific
IDs, and complete re-scaling with unit conversion.
Serial DAC Waveform plots the converted digital-to-analog data as a waveform time-correlated to other acquisition data, and view the
change in data over time.
Message to Analog, Analog to Message, Message to Message, Δ Message Time (identical message on same decoder), Time@Message
(time from trigger)
Serial Message may be defined by "ID =" (where applicable) and user-defined DATA with condition <=, <, =, >, >=, <>, IN RANGE, or OUT OF
RANGE in any location in up to 2048 bits of data.
Analog Signal may be defined by Slope (pos, neg), Level (abs or %) with Hysteresis setting. Holdoff may be set on the Analog Signal by
either Time or Events (up to 1000) to preclude unwanted measurements.
Number of Messages, Message Bit Rate, Message Bus Load %
Serial Message may be defined by "ID =" (where applicable) and user-defined "DATA <=, <, =, >, >=, <>, in range, out of range" in any
location in up to 2048 bits of data.
pility
Create up to four simultaneous Eye Diagrams (one per Serial Decoder) of the physical layer signal(s). Eye Style selectable as color- or analog-persisted. Eye Saturation adjustable from 0 to 100%.
Eye Height, Eye Width, (Number of) Mask Hits
Create a custom Mask using the free Teledyne LeCroy MaskMaker software utility. Store custom masks for later recall and use.
Mask Failure Indication ON or OFF (ON = indicated with a red circle).
Mask Failure Location trace waveform displayed and interactive with Eye Mask failure table. Supports STOP trigger on Mask Failure.

UART AND RS232

Key Features

Completely configurable UART-byte structure

Customizable Message Frame (multiple bytes in one Frame) for proprietary protocol triggering

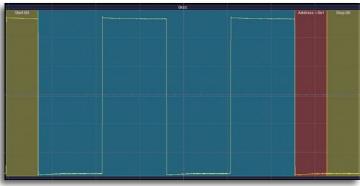
Supports 9-bit "address" or "wakeup" mode in byte definition (triggering and decoding)

Supports up to 16-bit Data words for decoding

Binary, Hexadecimal, ASCII or Decimal decoding

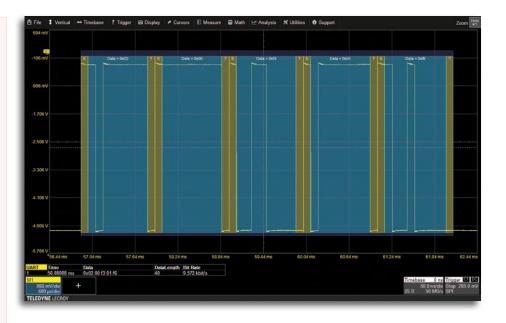
Polarity either IdleLow or IdleHigh

Use analog or digital (MSO) inputs for acquisition and triggering.



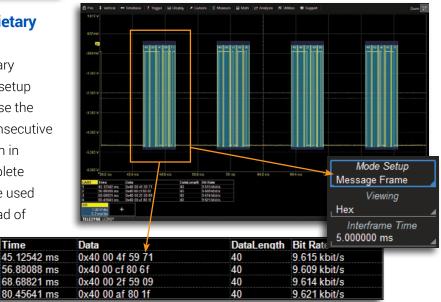
Interframe Message Time Setup for Proprietary UART-based Protocols

UART byte-based serial data signals with proprietary formats are often used. Given the flexibility of the setup of our trigger and decoder, it is often possible to use the Interframe Message Time Setup to "packetize" consecutive bytes into one Message Frame, and then trigger on in serial data in a particular byte location in the complete multi-byte Frame. Then, the UART decoder may be used to decode information as a complete Frame instead of individual Bytes.



9-bit "Address" or "Wakeup" Triggering

Most UART triggers assume a maximum of 8 data bits (excluding stop/start and parity bits) in a single byte. However, our solution supports 9-bit data bytes for situations in which a UART protocol is utilized for Address, Wakeup or other communication to another peripheral, preceding the normal serial data byte transmission.



	UART-RS232bus TD and UART-RS232bus TDME				
Definition					
Source and Protocol Setup	For UART: Select Source for Data Select BitRate Select # Data Bits (5-9) Select Parity (Odd, Even, None) Select # Stop Bits (1, 1.5, 2) Select Bit Order (MSB or LSB) Select Polarity (IdleLow or IdleHigh)	For RS-232: Select Source for Data Select BitRate Select # Data Bits (5-8) Select Parity (Odd, Even, None) Select # Stop Bits (1, 1.5, 2)			
Trigger Capability					
Format	Hexadecimal or Binary				
Trigger Setup	Trigger on DATA or Parity Error				
DATA Setup	Hexadecimal: # Data Bytes = 0 to 12. Data can be defined by nibble. Binary: Any combination of 0,1, or X for 1-96 bits Data pattern can be set to start at any location in the 12 B "Frame" definition permits definition of UART byte packets value. In this mode, a 12-bit Data pattern can be defined a	into a single long message package through a user-defined "Interframe Time"			
DATA Condition Setup	<=, <, =, >, >=, <>, IN RANGE, OUT OF RANGE, or DON'T CARE				
Bit Rates	User-defined to any nominal value from 300 b/s to 10 Mb/s				
Decode + Search	Capability				
Format	Hexadecimal, Binary, ASCII				
Decode Setup	Threshold definition required. Default is to Percent amplitude. Select BitRate, # Data Bits (5 to 16), Parity (NONE, ODD, EVEN), # Stop Bits (1 or 2), Bit Order (MSB or LSB), and Polarity (IDLE HIGH or IDLE LOW) (for RS-232, no Bit Order or Polarity setup). Frame definition permits definition of UART byte packets into a single long (decoded) message package through a user-defined "Interframe Time" value.				
Decode Input	Any analog Channel, Memory or Math trace, and any Digita	al trace.			
# of Decodes	Up to 4 buses may be decoded at one time. In addition, zooms can be displayed (with decoded information).				
Visual Aid	Color Coding for START Bit, STOP Bit, PARITY Bit, and DATA bytes. Decode information is intelligently annotated based on timebase setting, and overlaid on acquired waveform.				
Table Configure, Export Table	Display 1 to 20 rows of decoded information for up to 4 different protocols or decodes in time order in a single table. Displayed information includes Index, Timestamp, and other various protocol-specific information. Table permits scrolling, touch to zoom, export to .csv file, and special display of long data or other patterns.				
Pattern Search	Search for Previous or Next ERROR or DATA Byte in hexadecimal format.				

UART-RS232bus TDME only

Measure / Graph C	apability	
Serial Data Digital-to-Analog Conversion (DAC)	Message to Value measurement parameter extracts and converts a specified portion of the data in an up to 2048 byte data/payload in the serial message and displays it as an analog decimal value. Supports different data encoding formats, message filtering to specific IDs, and complete re-scaling with unit conversion. Serial DAC Waveform plots the converted digital-to-analog data as a waveform time-correlated to other acquisition data, and view the change in data over time.	
Timing Measurements	Message to Analog, Analog to Message, Message to Message, ΔMessage Time (identical message on same decoder), Time@Message (time from trigger) Serial Message may be defined by "ID =" (where applicable) and user-defined DATA with condition <=, <, =, >, >=, <>, IN RANGE, or OUT OF RANGE in any location in up to 2048 bits of data. Analog Signal may be defined by Slope (pos, neg), Level (abs or %) with Hysteresis setting. Holdoff may be set on the Analog Signal by either Time or Events (up to 1000) to preclude unwanted measurements.	
Bus Status Measurements	Number of Messages, Message Bit Rate, Message Bus Load % Serial Message may be defined by "ID =" (where applicable) and user-defined "DATA <=, <, =, >, >=, <>, in range, out of range" in any location in up to 2048 bits of data.	
Eye Diagram Capa	bility	
Setup	Create up to four simultaneous Eye Diagrams (one per Serial Decoder) of the physical layer signal(s). Eye Style selectable as color- or analog-persisted. Eye Saturation adjustable from 0 to 100%.	
Eye Parameters	Eye Height, Eye Width, (Number of) Mask Hits	
Eye Mask	Create a custom Mask using the free Teledyne LeCroy MaskMaker software utility. Store custom masks for later recall and use.	
Failure Indication and Location	"Mask Failure Indication ON or OFF (ON = indicated with a red circle). Mask Failure Location trace waveform displayed and interactive with Eye Mask failure table. Supports STOP trigger on Mask Failure.	

CAN AND CAN FD

Key Features

Symbolic trigger setup, decode, and data extraction and graph setup using (customer-supplied) DBC file

Error-frame red color decode highlight

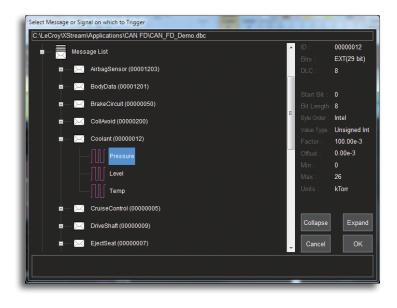
DATA trigger pattern setup can be less than full bytes/nibbles and can be spread across bytes

Conditional ID definition (<, <=, =, >, >=, <>, IN RANGE, OUT of RANGE)

Supports 29-bit GM CAN Priority ID, Source ID, Parameter ID trigger and decode

Supports J1939 decode



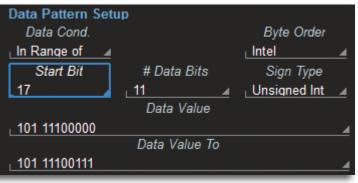


Symbolic (DBC) File Support

CAN and CAN FD Symbolic decode options both support use of a customer-supplied DBC file for signal selection for triggering and CAN to Value serial data DAC setup. Additionally, the decode annotation is in Symbolic format as well, with complete message and signal structures described.

Trigger Flexibly Across Data Bytes

CAN remains the most used vehicle serial data bus. Many vehicle bus software architectures are very message dense, and data for a single message is spread across multiple data bytes. The hexadecimal and measurement toolsets permit isolation of specific bit-level data patterns in one or more data bytes, e.g., data location in bits 17-28 in data bytes 3, 4, and 5. This provides significant advantages in isolating the exact information or behavior you need. Symbolic message/signal setup is even simpler.



C Definition Protocol Setup Trigger Capability	ANbus TD, CAN FDbus TD, ANbus TDME, CAN FDbus TDME ANbus and CAN FDbus: Select Source. Select BitRate and Data BitRate.	CANbus TDME Symbolic, CAN FDbus TDME Symbolic
Protocol Setup CA CA Trigger Capability	ANbus and CAN FDbus: Select Source. Select BitRate and Data BitRate.	
Trigger Capability	Andus and CANT Dous. Select Source. Select Dithate and Data Dithate.	CAN EDbus Only: Salast Frama Type (EDL) Both (X) CAN Standard (0) or
He	AN FD (1). For CAN FD, select ISO FRAME, and BR Select (BRS) Both(X),	
Format		ID: Symbolic, Hexadecimal, Binary.
i viinat	exadecimal or Binary.	DATA: Symbolic, Hexadecimal, Binary.
		ID and DATA can be set up with different formats.
	rigger on ID, ID+DATA, REMOTE, ERROR, or ALL (Data, Remote, or Error F et Requested (Bit) Sampling Point from 20 to 90% (Basic) or set values fi	
He Sp ID Setup <> St	exadecimal or Binary: pecify STD (11-bit) or EXT (29-bit) ID(s) with condition of <=, <, =, >, >=, >, IN RANGE, OUT OF RANGE, or DON'T CARE. upports triggering when both 11-bit and 29-bit IDs are present on the us.	Symbolic: Specify a Message to trigger on using customer supplied DBC database file. Choose from list sorted by Node, Message, or Signal. GM CAN compatible (Priority ID, Parameter ID, Source ID). Hexadecimal or Binary: Specify STD (11-bit) or EXT (29-bit) ID(s) with condition of <=, <, =, >, >=, <>, IN RANGE, OUT OF RANGE, or DON'T CARE. Supports triggering when both 11-bit and 29-bit IDs are present on the bus.
ca DATA Setup Da in	exadecimal: # Data Bytes = 0 to 8 (CAN) or 0-12. (CAN FD) Data bytes an be defined by nibble. inary: Any combination of 0,1, or X for 1-64 (CAN) or 0-96 (CAN FD) bits ata pattern can be any length and can be set to start at any location the up to 8 (CAN) or 12 (CAN FD) Byte / 64 (CAN) or 96 (CAN FD) bit equence. Byte Order Intel or Motorola format, Signed or Unsigned Data.	Symbolic: Message+Signal with Signal value set in scaled units as defined in customer supplied DBC database file. Hexadecimal: # Data Bytes = 0 to 8 (CAN) or 0-12. (CAN FD) Data bytes can be defined by nibble. Binary: Any combination of 0,1, or X for 1-64 (CAN) or 0-96 (CAN FD) bits Data pattern can be any length and can be set to start at any location in the up to 8 (CAN) or 12 (CAN FD) Byte / 64 (CAN) or 96 (CAN FD) bit sequence. Byte Order Intel or Motorola format, Signed or Unsigned Data.
	=, <, =, >, >=, <>, IN RANGE, OUT OF RANGE, or DON'T CARE	
	elect any combination of All Error Frames, Stuff Bit Errors, CRC Mismatc ounter Parity Error.	h Errors. In CAN FDbus, also select Stuffbit Counter Error and Stuffbit
Remote Frame	upported for ID. Capability identical to ID Condition Setup.	
		Mb/s pre-defined nominal values, or user-defined from 10 kb/s to 1 Mb/s.
	AN FD Data Bit Rate: 0.5, 1.0, 1.5, 2.0, 5.0, 8.0, or 10 Mb/s pre-defined no ny analog Channel or Digital input, or the EXT input.	minal values, or user-defined to any nominal value from 0.5 to 10 Mb/s.
Decode + Search Cap	pability	
Format He		Symbolic (Message and Signal level) or Hexadecimal. Symbolic decode requires user-provided DBC database file.
	hreshold definition required. Default is to Percent amplitude. Select bit ra	te(s).
	ny analog Channel, Memory or Math trace, and any Digital trace. p to 4 buses may be decoded at one time. In addition, zooms can be dis	splayed (with decoded information).
Cc AC Visual Aid wh De	olor Coding for FRAME, ID, IDE, EDL, BRS, ESI, RTR, DLC, DATA, CRC, CK, STUFF BITS, BIT INDEX, and ERRORS. Error Frames are decoded thenever possible, with uncorrupted portions decoded to Identify Type. ecode information is intelligently annotated based on timebase setting, and a variated an accurate decoded.	Color Coding for FRAME, ID, IDE, EDL, BRS, ESI, RTR, DLC, DATA, CRC, ACK, STUFF BITS, BIT INDEX and ERRORS. Symbolic includes textual Message name and physical Signal value with units. Error Frames are decoded whenever possible, with uncorrupted portions decoded to Identify Type. Decode information is intelligently annotated based on timebase setting, and overlaid on acquired waveform.
Expert Table	isplay 1 to 20 rows of decoded information for up to 4 different protocols	
	earch for Previous or Next Index, ID, IDE, DLC, DATA, and STATUS	
	CANING TRAF CANING TRAF Symbolic CAN EDDu	TOME CAN EDAge TOME Symbolic only
Measure / Graph Cap	CANbus TDME, CANbus TDME Symbolic, CAN FDbus	TDME, CAN FDDUS TDME Symbolic only
Serial Data M Digital-to-Analog Conversion (DAC)	Message to Value measurement parameter extracts and converts a s	specified portion of the data in an up to 2048 byte data/payload in the different data encoding formats, message filtering to specific IDs, and the converted digital-to-analog data as a waveform time-correlated
Measurements (t IN or	Message to Analog, Analog to Message, Message to Message, AMe time from trigger). Serial Message may be defined by "ID =" (where a N RANGE, or OUT OF RANGE in any location in up to 2048 bits of dat or %) with Hysteresis setting. Holdoff may be set on the Analog Signa neasurements.	a. Analog Signal may be defined by Slope (pos, neg), Level (abs
	lumber of Messages, Message Bit Rate, Message Bus Load % . Ser lefined "DATA <=, <, =, >, >=, <>, in range, out of range" in any location	
Eye Diagram Capabili		
Setup C	Treate up to four simultaneous Eye Diagrams (one per Serial Decoder Analog-persisted. Eye Saturation adjustable from 0 to 100%.) of the physical layer signal(s). Eye Style selectable as color- or
	Eye Height, Eye Width, (Number of) Mask Hits	
	Create a custom Mask using the free Teledyne LeCroy MaskMaker sc	ftware utility. Store custom masks for later recall and use.
-	Mask Failure Indication ON or OFF (ON = indicated with a red circle).	Eye Mask failure table. Supports STOP trigger on Mask Failure.

LIN

Key Features

LIN 1.3, 2.x and J2602 support

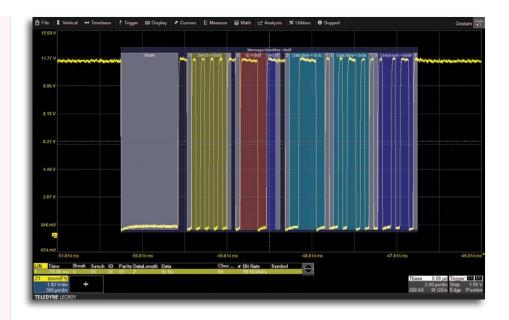
Break (Start of Message), ID, ID+DATA, and Error Frame triggers

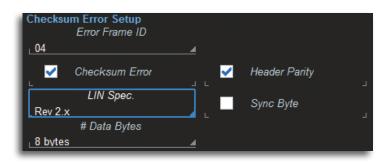
Error-frame red color decode highlight

Error-frame trigger can include some or all of Checksum, Header Parity, or Sync Byte types.

Conditional ID definition (<, <=, =, >, >=, <>, IN RANGE, OUT of RANGE)

Supports decode of buses with mixed LIN version traffic



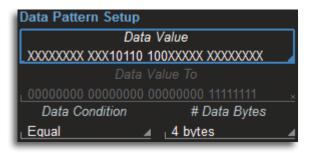


Flexible Error Frame Trigger

Select to trigger on any combination of Checksum, Header Parity, or Sync Byte error frame types. Additional, Checksum Error allows further definition for Frame ID, LIN Version, and Number of Data Bytes.

Trigger Flexibly Across Data Bytes

Many vehicle bus software architectures are very message dense, and data for a single message is spread across multiple data bytes. Our LIN trigger and measurement toolsets permit isolation of specific bit-level data patterns in one or more data bytes, e.g., data location in bits 18-26 in data bytes 2 and 3. This provides significant advantages in isolating the exact information or behavior you need.



	LINbus TD and LINbus TDME
Definition	
Source and Protocol Setup	Select Source. Select BitRate.
Trigger Capability	
Format	Hexadecimal or Binary
Trigger Setup	Trigger on (Sync) Break (Start of Message), Frame ID, Frame ID+DATA, Error Frame (Any combination of Checksum, Header Parity, or Sync Byte error frames)
ADDRESS Setup	Specify one ADDRESS with condition of <=, <, =, >, >=, <>, IN RANGE, OUT OF RANGE, or DON'T CARE.
DATA Setup	Hexadecimal: # Data Bytes = 0 to 8. Data can be defined by nibble. Binary: Any combination of 0,1, or X for 1-64 bits Data pattern can be any length and can be set to start at any location in the up to 8 Byte / 64 bit sequence.
DATA Condition Setup	<=, <, =, >, >=, <>, IN RANGE, OUT OF RANGE, or DON'T CARE
Error Setup	Select any combination of Checksum Error, Header Parity, or Sync Byte types. Checksum Error Setup for Frame ID, LIN version, and # Data Bytes
Bit Rates	1.2, 2.4, 4.8, 9.6, 10.417, 19.2 kb/s pre-defined nominal values, or user-defined to any nominal value from 300 b/s - 20 kb/s.
Trigger Input	Any analog Channel or Digital input, or the EXT input.
Decode + Search (Capability
Format	Hexadecimal, Binary
Decode Setup	Threshold definition required. Default is to Percent amplitude. Select BitRate. Select LIN version (1.3, 2.x, J2602, ALL). Decodes LIN messages on busses with mixed LIN versions
Decode Input	Any analog Channel, Memory or Math trace, and any Digital trace.
# of Decodes	Up to 4 buses may be decoded at one time. In addition, zooms can be displayed (with decoded information).
Visual Aid	Color Coding for FRAME, BREAK, START/STOP bits, SYNCH bits, ID, ID Parity, DATA, CRC. Decode information is intelligently annotated based on timebase setting, and overlaid on acquired waveform.
Table Configure, Export Table	Display 1 to 20 rows of decoded information for up to 4 different protocols or decodes in time order in a single table. Displayed information includes Index, Timestamp, and other various protocol-specific information. Table permits scrolling, touch to zoom, export to .csv file, and special display of long data or other patterns.
Pattern Search	Search by Previous or Next Frame, Next ID (hexadecimal format), or Next Error Frame.

	LINbus TDME only
Measure / Graph C	apability
Serial Data Digital-to-Analog Conversion (DAC)	Message to Value measurement parameter extracts and converts a specified portion of the data in an up to 2048 byte data/payload in the serial message and displays it as an analog decimal value. Supports different data encoding formats, message filtering to specific IDs, and complete re-scaling with unit conversion. Serial DAC Waveform plots the converted digital-to-analog data as a waveform time-correlated to other acquisition data, and view the change in data over time.
Timing Measurements	Message to Analog, Analog to Message, Message to Message, ΔMessage Time (identical message on same decoder), Time@Message (time from trigger) Serial Message may be defined by "ID =" (where applicable) and user-defined DATA with condition <=, <, =, >, >=, <>, IN RANGE, or OUT OF RANGE in any location in up to 2048 bits of data. Analog Signal may be defined by Slope (pos, neg), Level (abs or %) with Hysteresis setting. Holdoff may be set on the Analog Signal by either Time or Events (up to 1000) to preclude unwanted measurements.
Bus Status Measurements	Number of Messages, Message Bit Rate, Message Bus Load % Serial Message may be defined by "ID =" (where applicable) and user-defined "DATA <=, <, =, >, >=, <>, in range, out of range" in any location in up to 2048 bits of data.
Eye Diagram Capa	bility
Setup	Create up to four simultaneous Eye Diagrams (one per Serial Decoder) of the physical layer signal(s). Eye Style selectable as color- or analog-persisted. Eye Saturation adjustable from 0 to 100%.
Eye Parameters	Eye Height, Eye Width, (Number of) Mask Hits
Eye Mask	Create a custom Mask using the free Teledyne LeCroy MaskMaker software utility. Store custom masks for later recall and use.
Failure Indication and Location	Mask Failure Indication ON or OFF (ON = indicated with a red circle). Mask Failure Location trace waveform displayed and interactive with Eye Mask failure table. Supports STOP trigger on Mask Failure.

FLEXRAY

Key Features

The most comprehensive oscilloscopebased FlexRay solution

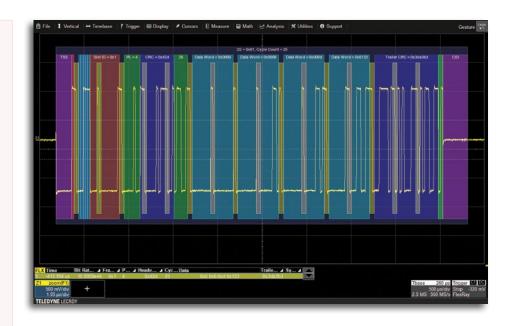
Supports triggering for.

- Frame ID (Static and Dynamic)
- Frame Cycle Count
- Frame Qualifiers
- Symbols
- Errors

Physical Layer Measurements

- Propagation Delay
- Asymmetric Delay
- Truncation
- Jitter
- SI Voting

Supports 2.5, 5 and 10 Mb/s signals



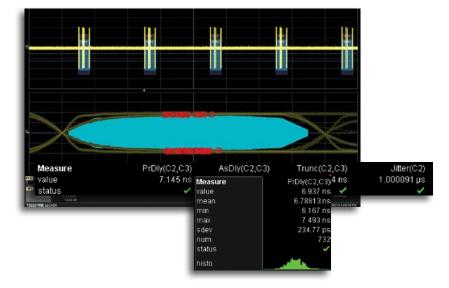
Trigger Type	Setup Format	Frame ID Setup Condition	Cycle Count Condition	Frame Qualifiers Payload Preamble	
TSS (Start) Frame	Binary	<u>In Range of</u> Value _3A5 ∡ To	Lequal Value L25	LOne	
Symbol	Hex	_ <u>3B7</u>	_63× Repetition 8 Factor ∡	One ▲ Startup Frame Don't Care	

Extensive Triggering Capabilities

Triggering on the complex FlexRay protocol is made easy. Set up a simple TSS (Start) symbol trigger with a single button press or trigger on any part of a FlexRay frame including ID, Cycle Count, Cycle Repetition Factor, and Frame Qualifier. FlexRay defined Symbols and Errors can also be incorporated into the trigger making it as simple or advanced as necessary. Conditional triggering can be set to trigger on any range of Frame IDs or Cycles.

Powerful Physical Layer Test

FlexRay eye diagram mask test overlays all the bits on FlexRay signal in an eye diagram with user-selected masks. Trigger on a specific Frame ID or range of IDs, or filter one long acquisition specific IDs, and show only those messages in the eye diagram. Supports SI Voting. Key timing parameters like Propagation Delay, Asymmetric Delay, Truncation and Jitter help you understand how signals propagate along the channel. Use statistics and histicons for deeper insight..



	FLEXRAYbus TD and FLEXRAYbus TDMP
Definition	
Source and Protocol Setup	Select Source. Select BitRate. Select FlexRay Channel A or Channel B.
Trigger Capability	
Format	Hexadecimal or Binary for Frame ID. Decimal for Cycle Count.
Trigger Setup	Trigger on TSS (Start), Frame ID, Cycle Count, Symbols, and Errors
FRAME Setup	Specify Frame ID(s) in Hexadecimal or Binary with condition of <=, <, =, >, >=, <>, IN RANGE, OUT OF RANGE, or DON'T CARE. Specify Cycle Count from 0 to 63 with condition of <=, <, =, >, >=, <>, IN RANGE, OUT OF RANGE, or DON'T CARE. Specify Repetition Factor as 1, 2, 4, 8, 16, 32, or 64. Specify various Frame Qualifiers (Payload Preamble, Null Frame, Sync Frame, and Startup Frame) as 0, 1, or X (don't care).
DATA Setup	Hexadecimal: # Data Bytes = 0 to 8. Data can be defined by nibble. Binary: Any combination of 0,1, or X for 1-64 bits Data pattern can be any length and can be set to start at any location in the up to 8 Byte / 64 bit sequence.
Error Setup	Trigger on any combination of the following errors: Frame Start Sequence (FSS) Error – triggers when the logic high time between the TSS and the first byte is too long. Byte Start Sequence (BSS) Error – triggers anytime the BSS pattern is not seen between bytes where expected. Frame End Sequence (FES) Error – triggers when the FS is not seen after the last byte. Header CRC Error, Payload CRC Error (select Payload Channel A or B).
Symbol Trigger	Trigger on any combination of the following: Channel Idle Delimiter (CID) Symbol, Collision Avoidance Symbol (CAS) and/or Media Access Test Symbol (MTS), or Wakeup Pattern (WUP)
Bit Rates	2.5, 5, or 10 Mb/s pre-defined nominal values, or user-defined nominal values in 1 Mb/s increments
Trigger Input	Any analog Channel or the EXT input.
Decode + Search	Capability
Format	Hexadecimal, excepting Cycle Count (Decimal)
Decode Setup	Threshold definition required for High and Low levels. Default is to Absolute (in volts) amplitude. Select Channel (A or B).
Decode Input	Any analog Channel, Memory or Math trace, and any Digital trace.
# of Decodes	Up to 4 buses may be decoded at one time. In addition, zooms can be displayed (with decoded information).
Visual Aid	Color Coding for FRAME, TSS, CID, FSS, Frame Qualifiers, Slot ID, Payload Length, Header CRC, Cycle Count, Data, BSS, Payload CRC and FES. Decode information is intelligently annotated based on timebase setting, and overlaid on acquired waveform.
Table Configure, Export Table	Display 1 to 20 rows of decoded information for up to 4 different protocols or decodes in time order in a single table. Displayed information includes Index, Timestamp, and other various protocol-specific information. Table permits scrolling, touch to zoom, export to .csv file, and special display of long data or other patterns.
Pattern Search	Search by Previous or Next Frame, Next ID (hexadecimal format), or Next Error Frame.

FLEXRAYbus TDMP only

Measure / Graph C	apability			
Serial Data Digital-to-Analog Conversion (DAC)	Message to Value measurement parameter extracts and converts a specified portion of the data in an up to 2048 byte data/payload in the serial message and displays it as an analog decimal value. Supports different data encoding formats, message filtering to specific IDs, and complete re-scaling with unit conversion. Serial DAC Waveform plots the converted digital-to-analog data as a waveform time-correlated to other acquisition data, and view the change in data over time.			
Timing Measurements	Message to Analog, Analog to Message, Message to Message, ΔMessage Time (identical message on same decoder), Time@Message (time from trigger). Serial Message may be defined by "ID =" (where applicable) and user-defined DATA with condition <=, <, =, >, >=, <>, IN RANGE, or OUT OF RANGE in any location in up to 2048 bits of data. Analog Signal may be defined by Slope (pos, neg), Level (abs or %) with Hysteresis setting. Holdoff may be set on the Analog Signal by either Time or Events (up to 1000) to preclude unwanted measurements.			
Bus Status Measurements	Number of Messages, Message Bit Rate, Message Bus Load %. Serial Message may be defined by "ID =" (where applicable) and user- defined "DATA <=, <, =, >, >=, <>, in range, out of range" in any location in up to 2048 bits of data.			
Eye Diagram Capa	bility			
Setup	Create up to four simultaneous Eye Diagrams (one per Serial Decoder) of the physical layer signal(s). Eye Style selectable as color- or analog-persisted. Eye Saturation adjustable from 0 to 100%.			
Eye Parameters	FlexRay PHY: Supports SI Voting (ON or OFF). With SI Voting ON, with voting selection for Positive Bit Length, Negative Bit Length (or both), and Filtered Input is possible. With SI Voting OFF, Any combination of Propagation Delay, Asymmetric Delay, Frame TSS Length Change, or Jitter. Eye Diagram: Eye Height, Eye Width, (Number of) Mask Hits			
Eye Mask	Create a custom Mask using the free Teledyne LeCroy MaskMaker software utility. Store custom masks for later recall and use. Standard FlexRay TP1, TP1 Bus Driver, TP11, and TP11 Active Star are also provided.			
Failure Indication and Location	Mask Failure Indication ON or OFF (ON = indicated with a red circle). Mask Failure Location trace waveform displayed and interactive with Eye Mask failure table. Supports STOP trigger on Mask Failure.			

ARINC 429

Key Features

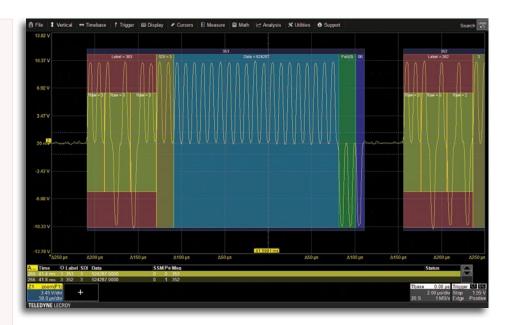
Symbolic decode with user-provided ULDF database file

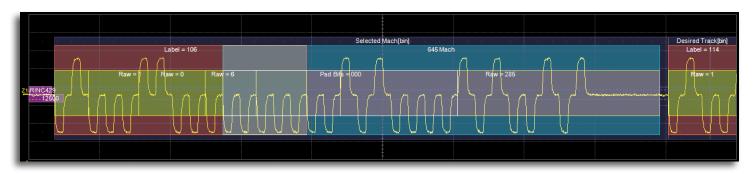
Decode Viewing Control Selection

- 8+24
- 8+2+19+2+1
- User-defined

Decode Annotation includes:

- Frame
- ID
- Label
- Raw Bits
- SDI
- Data
- SSM
- Parity
- Symbolic Message and Symbols





Symbolic Decode Transparent Overlay

A unique and powerful way to view decoded data. Using a user-provided ULDF file, the label and equipment ID fields can be displayed in an intuitive and easy to interpret way. The ULDF Label file is a Comma Separated Variable (CSV) file that contains the ARINC429 token definitions. Any text editor can be used to create or modify the Label file, and there is no limitation as to how many signals can be defined for a given Label. Here, the specified converted data is Selected Mach = 645 Mach.

Symbolic Decode Protocol Table

Symbol data is then displayed in the protocol table. Quickly view valuable information for each ARINC 429 word, such as Label, SDI, Data, SSM, Parity, and Symbolic Message.

Data	SS	M Pa Msg		Symbols	Bits/Resol	Pad
	645 Mach		Selected Mach[bin]		12 / 1.000000	
1	7.6 Degrees		Desired Track[bin]		12/0.050000	1
55	0 mDegrees		VS/FPA Val[bin]		12/0.050000	į (
	96 None		TACAN Control[bin]	ĺ	12 / 1.000000	i (
	0.0 m		Metric Altitude[bcd]		4/0.250000	i (
	30.0 kg/m2		Appr Loc[bcd]		4/0.250000	É (
	5.0 kg/m2		DD5[bcd]		4/0.250000	É (
	1.01e+3		Spare[bin]		10/1.000000	46
	63		Spare[bin]		10/1.000000	50

	ARINC429Bus DSYMBOLIC and ARINC429Bus DMESYMBOLIC		
Definition			
Source and Protocol Setup	Select Source. Select BitRate.		
Decode + Search	Capability		
Format	Hexadecimal or Symbolic+Hexadecimal. Symbolic decode requires user-provided ULDF database file		
Decode Setup	Threshold definition required for High and Low levels. Default is to Absolute (in volts) amplitude. Select Viewing Control (8+24, 8+2+19+2+1, or User-defined).		
Decode Input	Any analog Channel, Memory or Math trace, and any Digital trace.		
# of Decodes	Up to 4 buses may be decoded at one time. In addition, zooms can be displayed (with decoded information).		
Visual Aid	Color coding for Frame, ID, Label, Raw Bits, SDI, Data, SSM, Parity. Decode information is intelligently annotated based on timebase setting, and overlaid on acquired waveform.		
Table Configure, Export Table	Display 1 to 20 rows of decoded information for up to 4 different protocols or decodes in time order in a single table. Displayed information includes Index, Timestamp, and other various protocol-specific information. Table permits scrolling, touch to zoom, export to .csv file, and special display of long data or other patterns.		
Pattern Search	Search for Previous or Next IDX, Time, OctalDigits, Label, SDI, Data, SSM, Parity, Msg, or Status		

	ARINC429Bus DMESYMBOLIC only
Measure / Graph C	apability
Serial Data Digital-to-Analog Conversion (DAC)	Message to Value measurement parameter extracts and converts a specified portion of the data in an up to 2048 byte data/payload in the serial message and displays it as an analog decimal value. Supports different data encoding formats, message filtering to specific IDs, and complete re-scaling with unit conversion.
	Serial DAC Waveform plots the converted digital-to-analog data as a waveform time-correlated to other acquisition data, and view the change in data over time.
Timing Measurements	Message to Analog, Analog to Message, Message to Message, ΔMessage Time (identical message on same decoder), Time@Message (time from trigger)
	Serial Message may be defined by "ID =" (where applicable) and user-defined DATA with condition <=, <, =, >, >=, <>, IN RANGE, or OUT OF RANGE in any location in up to 2048 bits of data.
	Analog Signal may be defined by Slope (pos, neg), Level (abs or %) with Hysteresis setting. Holdoff may be set on the Analog Signal by either Time or Events (up to 1000) to preclude unwanted measurements.
Bus Status Measurements	Number of Messages, Message Bit Rate, Message Bus Load %
	Serial Message may be defined by "ID =" (where applicable) and user-defined "DATA <=, <, =, >, >=, <>, in range, out of range" in any location in up to 2048 bits of data.
Eye Diagram Capa	bility
Setup	Create up to four simultaneous Eye Diagrams (one per Serial Decoder) of the physical layer signal(s). Eye Style selectable as color- or analog-persisted. Eye Saturation adjustable from 0 to 100%.
Eye Parameters	Eye Height, Eye Width, (Number of) Mask Hits
Eye Mask	Create a custom Mask using the free Teledyne LeCroy MaskMaker software utility. Store custom masks for later recall and use.
Failure Indication and Location	Mask Failure Indication ON or OFF (ON = indicated with a red circle). Mask Failure Location trace waveform displayed and interactive with Eye Mask failure table. Supports STOP trigger on Mask Failure.

USB 2.0

Key Features

Supports USB 2.0 Low, Full, or High speeds (1.x and 2.0)

Trigger on USB packet types:

- Token
- Data
- Handshake
- User-defined

Transaction triggering support

Comprehensive Protocol Error and Bus Event triggering

Comprehensive Search by Events, Packets, Transactions or Errors



Pattern Type	Category	Data Pattern 1 PID	O	R Data Pattern 2 PID Data0	OR	Data Pattern Data1	
Packet	Any Sync	Compare Type		Compare Type		Compare	 Type
Protocol Error	Token	Data Length	4	Data Value		Data Value	
Transaction	Data Handshake	Cmp. Opr. _Any Length		Cmp. Opr. At position	i	Cmp. Opr. =	At position 40 🛛 🔏
Bus Event	User Define			Data (1-16 Bytes) F54B		Data (1-16 E 52	Bytes)

The Most Comprehensive USB Trigger

Full support is provided for triggering on any type of Packet, even User-Defined Packets, with complete flexibility for address, endpoint, split type, hub, port, etc. Trigger on specific Data payloads in specific locations. OR any three Packets in a single trigger condition. Create a USB Transaction trigger with any allowed combination of Token, Data, Handshake, and User-Defined packets. Advanced capability like this is usually only found in a dedicated protocol analyzer!

Search and Zoom

The powerful search engine of the USB 1.x/2.0 decode package can quickly find an Event, Packet, Transaction, or Protocol Error. Search through a long record of decoded data by entering any of the 45 available search criteria by entering a value or simply finding the next occurrence. For example, search through a long record to find a glitch that is frequently occurring after each EOP.



	USB2bus TD and USB2bus TDME
Definition	
Source and Protocol Setup	Select for USB Low, Full, or High Speeds (1.x and 2.0). Select Source(s) (one or more, depending on Speed and probing system used). Select D+ and D- Voltage Levels (Low and Full Speeds only)
Trigger Capability	
Format	Hexadecimal or Binary
Trigger Setup	Trigger on Packet Type (Any, Token, Data, Handshake, or User-Defined), Protocol Error, Transaction (combine any allowable set of Token, Data, Handshake or User-defined Packet together in a Transaction), or Bus Event
PACKET Setup	Any Packet: Trigger on ANY SYNCH pattern. Token Packet: Trigger on ANY Token Packet. Select PREAMBLE/ERR Token Packet. Select SOF Token Packet with specific Frame Number. Select OUT, IN, SETUP, or PING Token Packet with a specific Address and Endpoint, or "don't care." Select SPLIT Special Token Packet with a specific SPLIT TYPE, HUB ADDR, PORT, S(speed/start), E(nd), and ET (for SPLIT type). Select USER-DEFINED. Trigger on any of three Token Packets of any type and trigger on them with an "OR" condition. Data Packet: Trigger on ANY Data Packet. Trigger on a single DATA0, DATA1, DATA2, or MDATA Data packet, with settings for Data Payload or Data Length, or trigger on any of up to three Data Packets of any type in an OR condition, with independent setup of Data Payload value or Data Length values. Handshake Packet: Trigger on aNY Handshake Packet. Trigger on a specific ACK, NAK, NYET, STALL or ERR Handshake Packet. Transaction Packet: Trigger on any USB Transaction - combine any allowable set of Token, Data, Handshake or User-Defined Packet together in a Transaction, and trigger when that set is detected
DATA Setup	In any DATA PACKET define up to three data conditions with OR logic. Data conditions may be Data Payload Pattern or Data Length for Any, DATA0, DATA1, DATA2, or MDATA types. Data Payload Pattern Setup (Hexadecimal): # Data Bytes = 1 to 16. Data can be defined by nibble. Data Payload Pattern Setup (Binary): Any combination of 0,1, or X for 1-128 bits. Data Payload Pattern start at any location in an up to 128 Byte / 1024 bit sequence. Data Length Setup: Hexadecimal; # Data Bytes = 0 to 1024.
DATA Cond. Setup	Data Payload: =, <>, or DON'T CARE. Data Length: <=, <, =, >, >=, <>, IN RANGE, OUT OF RANGE, or ANY LENGTH
Error Setup	Trigger on any ORed combination of PID/Check Error, CRC5 Error, CRC16 Error, Frame Length Error, Bad Data Toggle Error, or PID0 Error
Bus Event Setup	Trigger on any ORed combination of Reset, Resume, Suspend, or Chirp
Bit Rates	Low, Full or High-speed pre-defined values.
Trigger Input	USB 1.x and USB 2.0 (Low and Full Speed): Requires two inputs, using any analog Channels or the EXT input USB 2.0 (High Speed): Pre-defined channel, specific to each oscilloscope product line. Input must be with a suitable differential probe.
Decode + Search C	apability
Format	Hexadecimal USB 2.0 Link and Data Layer Protocol Decode
Decode Setup	Select Bus Speed (Low, Full, High). Select Probing Type (One Single-ended Probe, Two Single-ended Probes, Differential Probe).
Decode Input	Any analog Channel, Memory or Math trace, and any Digital trace.
# of Decodes	Up to 4 buses may be decoded at one time. In addition, zooms can be displayed (with decoded information).
Visual Aid	Color Coding for Transaction, Packet (Handshake, Token, or Data), Control Sequences (Synch bits, PID bits, Check bits, or EOP bits), Device Address, Endpoint, Data Payload, CRC5 o CRC7, Inter-packet Idle, Inter-transaction Idle. Decode information is intelligently annotated based on timebase setting, and overlaid on acquired waveform.
Table Configure, Export Table	Display 1 to 20 rows of decoded information for up to 4 different protocols or decodes in time order in a single table. Displayed information includes Index, Timestamp, and other various protocol-specific information. Table permits scrolling, touch to zoom, export t .csv file, and special display of long data or other patterns.
Pattern Search	Search for Event, Token Packet, Data Packet, Handshake Packet, Transaction Packet, or Protocol Error (45 unique conditions)
	USB2bus TDME only
Measure / Graph C	apability
Serial Data Digital-to-Analog Conversion (DAC)	Message to Value measurement parameter extracts and converts a specified portion of the data in an up to 2048 byte data/payload in the serial message and displays it as an analog decimal value. Supports different data encoding formats, message filtering to specific IDs, and complete re-scaling with unit conversion. Serial DAC Waveform plots the converted digital-to-analog data as a waveform time-correlated to other acquisition data, and view the change in data over time.
Timina	Manage to Analog Analog to Manage Manage to Manage AManage Time (identical manage on some deader) Time (Manage

Timing Measurements	Message to Analog, Analog to Message, Message to Message, △Message Time (identical message on same decoder), Time@Message (time from trigger). Serial Message may be defined by "ID =" (where applicable) and user-defined DATA with condition <=, <, =, >, >=, <>, IN RANGE, or OUT OF RANGE in any location in up to 2048 bits of data. Analog Signal may be defined by Slope (pos, neg), Level (abs or %) with Hysteresis setting. Holdoff may be set on the Analog Signal by either Time or Events (up to 1000) to preclude unwanted measurements.		
Bus Status Measurements	Number of Messages, Message Bit Rate, Message Bus Load %. Serial Message may be defined by "ID =" (where applicable) and user- defined "DATA <=, <, =, >, >=, <>, in range, out of range" in any location in up to 2048 bits of data.		
Eye Diagram Capa	bility		
Setup	Create up to four simultaneous Eye Diagrams (one per Serial Decoder) of the physical layer signal(s). Eye Style selectable as color- or analog-persisted. Eye Saturation adjustable from 0 to 100%.		
Eye Parameters	Eye Height, Eye Width, (Number of) Mask Hits		
Eye Mask	Create a custom Mask using the free Teledyne LeCroy MaskMaker software utility. Store custom masks for later recall and use.		
Failure Indication and Location	Mask Failure Indication ON or OFF (ON = indicated with a red circle). Mask Failure Location trace waveform displayed and interactive with Eye Mask failure table. Supports STOP trigger on Mask Failure.		

MIL-STD-1553

Key Features

Most comprehensive MIL-STD-1553 oscilloscope trigger available

- Transfers
- Command Words
- Data Words
- Status Words
- Error Words
- Response Times
- Intermessage Gap Times

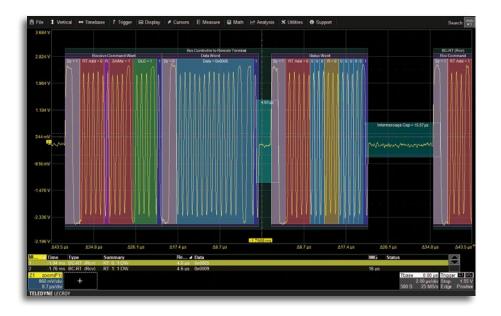
Conditional ADDRESS definition (<, <=, =, >, >=, <>, IN RANGE, OUT of RANGE)

Completely isolate a specific RT Address, Sub Address, Data Value, and Mode Code

Support for MIL-STD-1553 versions A and B

Highly Flexible and Powerful Triggering

The MIL-STD-1553 trigger can be configured at the transfer or word level to provide the right level of triggering. In addition, error triggers are able to locate the cause of protocol errors at either the transfer or word level. Word level triggering allows conditional RT Address and Sub Address entry.





	MIL-STD-1553 TD and MIL-STD-1553 TDME
Definition	
Source Setup	Select Source
Trigger Capabilit	'y
Format	Hexadecimal or Binary (Decimal for Word Count).
Trigger Setup	Trigger on ANY TRANSFER; a COMMAND WORD, STATUS WORD, DATA WORD, or ALL WORDS; an ERROR, a RESPONSE TIME, or an INTERMESSAGE GAP TIME. TRANSFERS may be further qualified by selecting the message type BC-RT, RT-BC, RT-RT, MODE COMMAND, MODE COMMAND & DATA (XMIT), MODE COMMAND AND DATA (RCV), various BROADCASTS (BC-RT(S), RT-RT(S), MODE COMMAND, and MODE COMMAND AND DATA)
Address Setup	For COMMAND WORD trigger specify 5-bit Remote Terminal (RT) Address ID(s) or Sub Address(es) with condition of <=, <, =, >, >=, <>, IN RANGE, OUT OF RANGE, or DON'T CARE; specify Transmit/Receive bit setting of 0, 1, or X (don't care). For STATUS WORD trigger, specify 5- bit RT Address(es) ID(s) with condition of <=, <, =, >, >=, <>, IN RANGE, OUT OF RANGE, or DON'T CARE; Specify Status Word bits as 0, 1, or X (don't care) for Message Error, Instrumentation, Service Request, Broadcast Command Received, Busy, Subsystem Flag, Dynamic Bus Control Acceptance, or Terminal Flag. For any TRANSFER containing an RT Address or Sub Address, setup is identical to that specified above. Settable in Hexadecimal or Binary format in all cases.

	MIL-STD-1553 TD and MIL-STD-1553 TDME (Cont'd)
DATA Setup	Data Word Count: In any TRANSFER, specify Data Word Count in decimal format up 32 data words. DATA WORD or TRANSFER Data Setup (Hexadecimal): # Data Bytes = up to 2 (one Data Word) byte length, settable by nibble. DATA WORD or TRANSFER Data Setup (Binary): Any combination of 0,1, or X for 1-16 bits. Data pattern can be set to start at any location in an up to 2 Byte / 16 bit sequence (in a DATA WORD) or an up to 64 Byte / 512 bit sequence (in a TRANSFER)
DATA Condition Setup	Data Word Count: <=, <, =, >, >=, or <> Data Setup: <=, <, =, >, >=, <>, IN RANGE, OUT OF RANGE, or DON'T CARE
Mode Command Setup	TRANSFER MODE COMMANDS and COMMAND WORDS may be qualified by selecting a Mode Code (0 to 31, with description) with a Mode Code condition of $<=, <, =, >, >=, $ or $<>$.
Status Setup	In any TRANSFER or STATUS WORD, select 0, 1, or X (don't care) for various Status Word bits. Select for: Message Error, Instrumentation Service Request, Broadcast Command Received, Busy, Subsystem Flag, Dynamic Bus Acceptance, and Terminal Flag
Error Setup	Select one or more Word Level or Transfer Level errors using a check box. Word Level error selection: Invalid Sync, Manchester Error, Idle Error, Parity Error. Transfer Level error selection: Bad Word Count, Address Mismatch, Non-contiguous Data, Sync Error.
Other Setups	Response Time Setup: Conditional Setup <, >, in range, out of range; Value Setup: 0 to 32.752 microseconds. Intermessage Gap Setup: Conditional Setup <, >, in range, out of range; Value Setup: 0 to 32.752 microseconds.
Bit Rates	1 Mb/s, pre-defined nominal value
Trigger Input	Any analog Channel or the EXT input.
Decode + Search	Capability
Format	Hexadecimal, Binary, Decimal (Binary not available for Address).
Decode Setup	Threshold definition required for High and Low levels. Default is to Absolute (in volts) amplitude. Select Table (Display) Mode (WORD or TRANSFER). Define Response Time and InterMessage Gap Time limits.
Decode Input	Any analog Channel, Memory or Math trace, and any Digital trace.
# of Decodes	Up to 4 buses may be decoded at one time. In addition, zooms can be displayed (with decoded information).
Visual Aid	Color Coding for Message, Word, Sync bits, RTA Address and SubAddress bits, Receive/Transmit bit, Data Count bits, Data (Payload) bytes and Single-bit Condition Codes, Reserved bits, Response Time Check and Inter-Message Gap Time, and Word and Transfer Level Error Codes. Decode information is intelligently annotated based on timebase setting, and overlaid on acquired waveform.
Table Configure, Export Table	Display 1 to 20 rows of decoded information for up to 4 different protocols or decodes in time order in a single table. Displayed information includes Index, Timestamp, and other various protocol-specific information. Table permits scrolling, touch to zoom, export to .csv file, and special display of long data or other patterns.
Pattern Search	Search for Previous or Next Index, Time, Message, Transaction, Type, Summary, Sync, RT Address, T/R, SubAddress, Count, ModeCode, Parity, Response Time, RT Address ACK, Message Error, Inst, SRQ, Reserved, Broadcase Rx, Busy, SubSystem Flag, Dynamic Bus Access, Terminal Flag, Data, IMG, or Status

MIL-STD-1553 TDME only

Measure / Graph C	apability
Serial Data Digital-to-Analog Conversion (DAC)	Message to Value measurement parameter extracts and converts a specified portion of the data in an up to 2048 byte data/payload in the serial message and displays it as an analog decimal value. Supports different data encoding formats, message filtering to specific IDs, and complete re-scaling with unit conversion. Serial DAC Waveform plots the converted digital-to-analog data as a waveform time-correlated to other acquisition data, and view the change in data over time.
Timing Measurements	Message to Analog, Analog to Message, Message to Message, ΔMessage Time (identical message on same decoder), Time@Message (time from trigger). Serial Message may be defined by "ID =" (where applicable) and user-defined DATA with condition <=, <, =, >, >=, <>, IN RANGE, or OUT OF RANGE in any location in up to 2048 bits of data. Analog Signal may be defined by Slope (pos, neg), Level (abs or %) with Hysteresis setting. Holdoff may be set on the Analog Signal by either Time or Events (up to 1000) to preclude unwanted measurements.
Bus Status Measurements	Number of Messages, Message Bit Rate, Message Bus Load %. Serial Message may be defined by "ID =" (where applicable) and user- defined "DATA <=, <, =, >, >=, <>, in range, out of range" in any location in up to 2048 bits of data.
Eye Diagram Capa	bility
Setup	Create up to four simultaneous Eye Diagrams (one per Serial Decoder) of the physical layer signal(s). Eye Style selectable as color- or analog-persisted. Eye Saturation adjustable from 0 to 100%.
Eye Parameters	Eye Height, Eye Width, (Number of) Mask Hits
Eye Mask	Create a custom Mask using the free Teledyne LeCroy MaskMaker software utility. Store custom masks for later recall and use.
Failure Indication and Location	"Mask Failure Indication ON or OFF (ON = indicated with a red circle). Mask Failure Location trace waveform displayed and interactive with Eye Mask failure table. Supports STOP trigger on Mask Failure.

Key Features

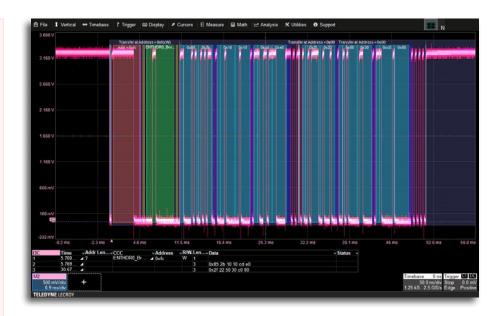
Supports triggers in SDR, HDR-DDR, and Legacy I2C operating modes

Provides triggering in "ANY" supported operating mode

Provides for complex triggering with defined Address, R/W (direction), Common Command Code, and Data.

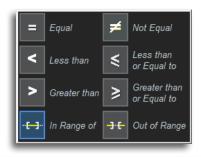
Conditional data definition (<, <=, =, >, >=, <>, INRANGE, OUT of RANGE).

Use analog or digital (MSO) inputs for acquisition and triggering.



Comprehensive and Powerful I3C Trigger

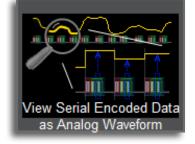
Capability is provided to permit triggering on any MIPI-defined allowable command sequence, and also on user-defined command sequences, with full capability to define specific data values or ranges of data values.





Extract Digital Sensor Data and Plot it as a Waveform

Embedded digital sensor data can be difficult to understand by reviewing hexadecimal values in a table. The I3Cbus TDME option provides ability to extract digital data from a defined data location in specific I3C messages and convert them to analog values that are then plotted to resemble an analog waveform.



	I3Cbus TD and I3Cbus TDME			
Definition				
Source Setup	Select Source for Clock and Data.			
Trigger Capability				
Format	Hexadecimal or Binary. ADDRESS and DATA can be set up with different formats.			
Trigger Setup	 Select from operating modes to trigger on the following: SDR: Start (S), Stop (P), Restart (SR), Direct Frame, Broadcast Frame, SDR Frame Length, Direct Address + CCC + Data, Broadcast Address + CCC + Data, SDR Errors HDR-DDR: Enter, Exit, Restart, Frame Length, Address + Cmd + Data, Preamble, Errors I2C Legacy: START, ReSTART, STOP, Missing ACK, ADDR, DATA, ADDR+DATA, ADDR+DATA FRAME LENGTH, EEPROM DATA TRANSFER Any Mode: Start (S), Stop (P), Restart (SR), Frame Length, Any Address + Data, Any Errors 			
ADDRESS Setup	S Setup Specify one ADDRESS with condition of "=". 7 or 10 bit ADDRESS supported with full Read, Write, or R/W="Don't Care" selectability on both 7 and 10 bit ADDRESSes. Choose to Trigger on address values that include/don't include R/W bit in address value.			
DATA Setup	ATA Setup Direct Address + CCC + Data, Broadcast Address + CCC + Data, ADDRESS+DATA, Any Address + Data, and HDR-DDR Address + Cm Data Trigger Types: Hexadecimal: # Data Bytes = 0 to 12. Data can be defined by nibble. Binary: Any combination of 0,1, or X for 1-96 bits Data pattern can be set to start at any location in the 12 Byte / 96 bit sequence.			
DATA Conditions	<=, <, =, >, >=, <>, IN RANGE, OUT OF RANGE, or DON'T CARE			
ACK Conditions	For any Direct Address + CCC + Data, Broadcast Address + CCC + Data, HDR-DDR + Cmd + Data, Addr + Data, or Any Addr + Data setup, select an ACK Condition of ACK, NO ACK, and DON'T CARE.			
Bit Rates	Full range of speeds for I3C specification for SDR, HDR-DDR, and I2C legacy operation modes.			
Trigger Input	Any analog Channel or Digital input, or the EXT input. Clock may be input to EXT to conserve available analog Channels.			
Decode + Search (Capability			
Format	Hexadecimal, Binary, ASCII			
Decode Setup	Threshold definition required. Defavult is to Percent amplitude. Choose to Decode address values including/not including the R/W bit in address value.			
Decode Input	Any analog Channel, Memory or Math trace, and any Digital trace. Clock channel may be turned OFF and data will still decode (reduces screen clutter).			
# of Decodes	Up to 4 buses may be decoded at one time. In addition, zooms can be displayed (with decoded information).			
Visual Aid	Color Coding for FRAME, START/ReSTART bit, ADDR, R/W, DATA, ACK, NACK, and STOP bit. Decode information is intelligently annotated based on timebase setting, and overlaid on acquired waveform.			
Table Configure, Export Table				
Pattern Search	Search for Previous or Next ADDRESS, PACKET, or DATA in hexadecimal format.			

	I3Cbus TDME only			
Measure / Graph C	apability			
Serial Data Digital-to-Analog Conversion (DAC)				
Timing Message to Analog, Analog to Message, Message to Message, ΔMessage Time (identical message on same decoder), Ti (time from trigger) Serial Message may be defined by "ID =" (where applicable) and user-defined DATA with condition <=, <, =, >, >=, <>, IN RAN: OF RANGE in any location in up to 2048 bits of data. Analog Signal may be defined by Slope (pos, neg), Level (abs or %) wit setting. Holdoff may be set on the Analog Signal by either Time or Events (up to 1000) to preclude unwanted measurement				
Bus Status Measurements	Number of Messages, Message Bit Rate, Message Bus Load %: Serial Message may be defined by "ID =" (where applicable) and user-defined "DATA <=, <, =, >, >=, <>, in range, out of range" in any location in up to 2048 bits of data.			
Eye Diagram Capa	bility			
Setup	Create up to four simultaneous Eye Diagrams (one per Serial Decoder) of the physical layer signal(s). Eye Style selectable as color- or analog-persisted. Eye Saturation adjustable from 0 to 100%.			
Eye Parameters	Eye Height, Eye Width, (Number of) Mask Hits.			
Eye Mask	Create a custom Mask using the free Teledyne LeCroy MaskMaker software utility. Store custom masks for later recall and use.			
Failure Indication and Location				

SPMI

Key Features

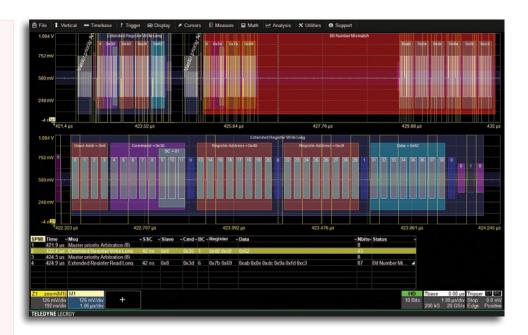
Trigger and Decode Commands, Command Sequences and Error Frames

User-defined Command Frame/ Sequence – trigger on non-standard frames.

Conditional data definition (<, <=, =, >, >=, <>, INRANGE, OUT of RANGE).

Use analog or digital (MSO) inputs for acquisition and triggering.

Full arbitration sequence support and for all sequences with pauses.



Comprehensive and Powerful SPMI Trigger

Capability is provided to permit triggering on any MIPI-defined allowable command sequence, and also on user-defined command sequences, with full capability to define specific data values or ranges of data values.

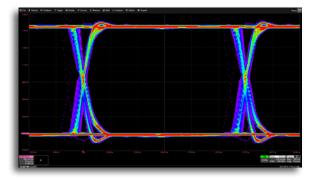






Eye Diagrams Identify Physical Layer Issues

Quickly create an eye diagram to intuitively show bit transitions and physical layer signal quality. In this 10 ms acquisition, it is easy to assess the signal quality and find the irregular bit transitions (faint purple lines).



	SPMIbus TD and SPMIbus TDME				
Definition					
Source and Protocol Setup	Select Source for Clock and Data.				
Trigger Capability					
Format	Hexadecimal or Binary				
Trigger Setup	Trigger on SSC, Command (Reset, Sleep, Shutdown, Wakeup), Command Sequence (Master Read, Master Write, Block Master Read, Block Slave Read, Extended Register Read, Extended Register Write, Extended Register Read Long, Extended Register Write Long, Register Read, Register Write, Register 0 Write, Authenticate, Transfer Bus Ownership, User-defined), or Errors (OR any combination of Command Parity Error, Acknowledgement Error, Data Parity Error, Frames Error).				
DATA Setup	Command Sequence Trigger Type: Hexadecimal: # Data Bytes = 0 to 16. Data can be defined by nibble. Binary: Any combination of 0,1, or X for 1-128 bits"				
DATA Condition Setup	<=, <, =, >, >=, <>, IN RANGE, OUT OF RANGE, or DON'T CARE				
Bit Rates	Any, up to 26 Mb/s. Auto-detected from clock signal.				
Trigger Input	Any analog Channel or Digital input, or the EXT input. Clock may be input to EXT to conserve available analog Channels.				
Decode + Search	Capability				
Format	Hexadecimal, with selection for Bit Viewing (Index or State).				
Decode Setup	Threshold definition required. Default is to Percent amplitude. Choose to Decode address values including/not including the R/W bit in address value.				
Decode Input	Any analog Channel, Memory or Math trace, and any Digital trace. Clock channel may be turned OFF and data will still decode (reduces screen clutter).				
# of Decoded Waveforms	Delta				
Visual Aid					
Table Configure, Export Table	ure, Display 1 to 20 rows of decoded information for up to 4 different protocols or decodes in time order in a single table. Displayed				
Pattern Search Basic search for Previous or Next INDEX, TIME, MESSAGE (value), SSC, SLAVE (id), COMMAND, BYTE COUNT CODE, REGIS (value), NBITS, or STATUS. Advanced search allows complex criteria using boolean and/or logic to combine up to three diffe searches.					

	SPMIbus TDME only				
Measure / Graph Capability					
Serial Data Digital-to-Analog Conversion (DAC)					
Timing Measurements	Message to Analog, Analog to Message, Message to Message, ΔMessage Time (identical message on same decoder), Time@Message (time from trigger) Serial Message may be defined by "ID =" (where applicable) and user-defined DATA with condition <=, <, =, >, >=, <>, IN RANGE, or OUT OF RANGE in any location in up to 2048 bits of data. Analog Signal may be defined by Slope (pos, neg), Level (abs or %) with Hysteresis setting. Holdoff may be set on the Analog Signal by either Time or Events (up to 1000) to preclude unwanted measurements.				
Bus Status Measurements	Number of Messages, Message Bit Rate, Message Bus Load % Serial Message may be defined by ""ID ="" (where applicable) and user-defined ""DATA <=, <, =, >, >=, <>, in range, out of range"" in any location in up to 2048 bits of data.				
Eye Diagram Capa	bility				
Setup	Create up to four simultaneous Eye Diagrams (one per Serial Decoder) of the physical layer signal(s). Eye Style selectable as color- or analog-persisted. Eye Saturation adjustable from 0 to 100%.				
Eye Parameters	Eye Height, Eye Width, (Number of) Mask Hits.				
Eye Mask	Create a custom Mask using the free Teledyne LeCroy MaskMaker software utility. Store custom masks for later recall and use.				
Failure Indication and Location	Mask Failure Indication ON or OFF (ON = indicated with a red circle). Mask Failure Location trace waveform displayed and interactive with Eye Mask failure table. Supports STOP trigger on Mask Failure.				

COMPATIBILITY

	WaveSurfer 3000z	WaveSurfer 4000HD	WaveSurfer 510, HDO4000A	WaveRunner 8000HD WaveRunner 9000
I ² C	EMB bundle		TD	TD, TDME
UART-RS232 USB 2.0 HSIC		EMB bundle	TD	TD, TDME
មិន UART-RS232			TD	TD, TDME
USB 2.0 HSIC	-	-	D	D
EMB bundle (I ² C, SPI, UART-RS232)	TD	TD	TD	TD, TDME
CAN	AUTO bundle		TD	TD, TDME, TDME Symbolic
CAN FD ⁶ FlexRay	TD	AUTO bundle	TD	TD, TDME, TDME Symbolic
FlexRay	TD		TD	TD, TDMP
	AUTO bundle		TD	TD, TDME
SENT	D	-	D	D
AUTO bundle	TD4	TD	TD ⁴	-
ARINC 429	-	-	D Symbolic	D Symbolic, DME Symbolic
MIL-STD-1553	-	-	TD	TD, TDME
SpaceWire	-	-	D	D
Ethernet (10/100Base-T)	-	-	D	D
Ethernet (10/100Base-T) USB 2.0	-	-	D	TD, TDME
DPHY	-	-	D	D, DP⁵
DigRF 3G	-	-	D	D, DM³
DigRF v4	-	-	D	D, DM³
SPMI	-	-	TD	TD, TDME
I ³ C	-	-	TD	TD, TDME
Audio (I ² S, LJ, RJ, TDM)	TD	TD	TD	TD, TDG
Manchester	-	-	D	D
NRZ	-	-	D	D, TD²

1 – DME, DME Symbolic, DMP, and DG packages are available for these models through the Teledyne LeCroy Service Department - consult factory

2 – Trigger and Decode are available through separately orderable options - consult factory

3 – Measure/Graph ("M") capability is available for these protocols through purchase of the Decoder ("D" option) and ProtoBus MAG Serial Debug Toolkit

(a separate orderable item)

Notoe

4 – AUTO bundle contains CAN and LIN trigger and decode ("TD") when purchased with WaveSurfer 3000z, and contains CAN, LIN and FlexRay trigger and decode ("TD") when purchased with WaveSurfer 10, and HDO4000A.

5 – Available on some bandwidth models only.

6 – CAN FDbus options support both CAN FD and the legacy CAN protocol

		HD06000A	WavePro HD	WaveMaster 8 Zi-B	LabMaster 10 Zi-A
	I ² C	TD, TDME	TD, TDME	TD, TDME	D, DME'
Embedded Computing	SPI	TD, TDME	TD, TDME	TD, TDME	D, DME'
dded Co	UART-RS232	TD, TDME	TD, TDME	TD, TDME	D, DME ¹
Imbe	USB 2.0 HSIC	D	D	D	D
	EMB bundle (I²C, SPI, UART-RS232)	TD, TDME	TD, TDME	TD, TDME	D, DME'
	CAN	TD, TDME, TDME Symbolic	TD, TDME, TDME Symbolic	TD, TDME, TDME Symbolic	D, DME¹, DME Symbolic¹
Automotive + Industrial	CAN FD⁰	TD, TDME, TDME Symbolic	TD, TDME, TDME Symbolic	TD, TDME, TDME Symbolic	D, DME¹, DME Symbolic¹
motive +	FlexRay	TD, TDMP	TD, TDMP	TD, TDMP	D, DMP ⁱ
Autor	LIN	TD, TDME	TD, TDME	TD, TDME	D, DME ¹
	SENT	D	D	D	D
	AUTO bundle	-	-	-	-
Avionics	ARINC 429	D Symbolic, DME Symbolic	D Symbolic, DME Symbolic	D Symbolic, DME Symbolic	D Symbolic, DME Symbolic¹
	MIL-STD-1553	TD, TDME	TD, TDME	TD, TDME	D, DME ¹
	SpaceWire	D	D	D	D
erals	Ethernet (10/100Base-T)	D	D	D	D
Peripherals	USB 2.0	TD, TDME	D, DME	D, DME	D, DME ¹
	DPHY	D	D, DP⁵	D, DP⁵	D, DP
	DigRF 3G	D, DM ³	D, DM³	D, DM³	D, DM ³
MIPI	DigRF v4	D, DM³	D, DM³	D, DM³	D, DM³
	SPMI	TD, TDME	TD, TDME	D, DME	D, DME'
	l ³ C	TD, TDME	TD, TDME	-	-
Other	Audio (I²S, LJ, RJ, TDM)	TD, TDG	TD, TDG	TD, TDG	D, DG ¹
	Manchester	D	D	D	D
	NRZ	D	D, TD²	D, TD²	TD ²

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(a separate orderable item)

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5 – Available on some bandwidth models only.

6 - CAN FDbus options support both CAN FD and the legacy CAN protocol

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Customer Service

Teledyne LeCroy oscilloscopes and probes are designed, built, and tested to ensure high reliability. In the unlikely event you experience difficulties, our digital oscilloscopes are fully warranted for three years and our probes are warranted for one year. This warranty includes:

• No charge for return shipping • Long-term 7-year support • Upgrade to latest software at no charge



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