

RF Specifications

Xilinx Spartan-6XC6SLX75 FPGA

Channels: 1 TX & 1 RX, Half or Full Duplex

Frequency range: 70 MHz to 6 GHz

Instantaneous Bandwidth: up to 56 MHz

SSB/LO Suppression: .35/50 dBc

3.5 GHz deg RMS

6 GHz deg RMS

IIP3 (at typical NF): .20 dBm

Power Output: >10 dBm

Receive Noise Figure: <8 dB

Conversion Performance and Clocks

ADC Sample Rate (Max.): 61.44 MS/s

ADC Resolution: 12 bits

ADC Wideband SFDR: 78 dBc

DAC Sample Rate (Max.): 61.44 MS/s

DAC Resolution: 12 bits

Host Sample Rate (16b): 61.44 MS/s

Frequency Accuracy: ± 2.0 ppm

W/ GPS Unlocked TCXO Reference: ± 75 ppb

W/ GPS Locked TCXO Reference: <1 ppb