## Specifications:

All specifications apply to the units after a temperature stabilization time of 20 minutes over an ambient range of $20^{\circ} \mathrm{C} \pm 5{ }^{\circ} \mathrm{C}$.

| Model | PB-505A |
| :---: | :---: |
| Power Source | AC Line: 115 VAC @ 60 Hz or 230 VAC @ 50 Hz (switchable) |
| Power Supplies | Fixed DC: +5 VDC 1.0 A max. (current limited); Ripple, $<5 \mathrm{mV}$ $\begin{gathered} \text { Variable + DC: }+1.3 \text { VDC } \pm 0.05 \mathrm{~V} @ 150 \mathrm{~mA} \text { to }+15 \mathrm{VDC} @ 500 \mathrm{~mA} \text {, Ripple }<5 \mathrm{mV} \\ \text { Variable - DC: -1.3 VDC } \pm 0.05 \mathrm{~V} @ 150 \mathrm{~mA} \text { to -15 VDC @ } 500 \mathrm{~mA} \text {, Ripple < } 5 \mathrm{mV} \\ \text { Fixed AC: } 12.6 \text { VAC center-tapped @ } 100 \mathrm{~mA} \end{gathered}$ |
| Binding Posts | 4 Power Supply Outputs: Ground, +5 VDC, Variable $\pm$ VDC, and AC Transformer central tapped (3 posts) |
| Pulsers | 2 Pushbutton-operated / open collector output pulsers. Each with 1 normally open output and 1 normally closed. Each output sinks up to 250 mA |
| Logic Probe | Detects Logic High, Logic Low and Single Shot events. <br> Logic High: 2.2 V (nominal) in TTL mode, $70 \%$ de VCC in CMOS mode. <br> Logic Low: 0.8 V in TTL mode, 30\% de VCC in CMOS mode. <br> Memory Mode: Detects single shor events and holds indication until Pulse/Mem is toggled |
| Digital Function Generator | Frequency Range: 0.1 Hz to $100 \mathrm{kHz}, 2$ ranges <br> Output Voltage: 0 to +10 Vp -p into $600 \Omega$ load (20 Vp-p in open circuit), short circuit protected <br> Output Impedance: $600 \Omega$, except TTL <br> Output Waveforms: Sine, Square, Triangle \& TTL <br> Sine Wave Distortion: < 3\% @ 1 kHz typical <br> TTL Pulse: Rise \& fall time: < 25 ns, drive 100 TTL <br> Square Wave: Rise \& fall time: $<0.5 \mu \mathrm{~s}$ |
| Logic Switches | 8 Logic switches select Logic High and Logic Low Logic Low Level: Ground Logic High Level: switchable between +5 V and the variable positive power supplies |
| Switches | 2 Single Pole Double Throw (SPDT) (SPDT) - uncommitted |
| Logic Indicators | 16 LED Bicolores: 8 red to indicate logic high and 8 green to indicate logic low Logic High Threshold: 2.2 V (nominal) in TTL/+5 V mode, $70 \%$ (nominal) of selected operating voltage in CMOS mode <br> Logic Low Threshold: 0.8 V (nominal) in TTL/+5 V mode, 30\% (nominal) of selected operating voltage in CMOS mode |
| Connectors | 2 BNC Connectors - uncommitted |
| Potentiometers | 2 Potentiometers: $1 \mathrm{k} \Omega$ and $10 \mathrm{k} \Omega$ - uncommitted |
| Speaker | $8 \Omega, 0.25 \mathrm{~W}$ - uncommitted |
| Displays | $2 B C D$ to 7-Segment display cricuits, 2 red LED and decoder/driver circuitry |
| Breadboard | Removable socket plate (PB-4) with 3,360 tie-points with 200 additional bus strip tie-points |
| Weight | $10 \mathrm{lbs}(4.5 \mathrm{~kg})$ |
| Dimensions | $4.5 \times 15 \times 10.75$ in (11.4 $\times 38.1 \times 27.3 \mathrm{~cm})(H \times W \times D)$ |
| Warranty | 3 Years |

## Contact Information:

Toll Free: 800-572-1028 (US only)
Phone: 714-221-9330Fax: 714-921-9849
22820 Savi Ranch Parkway
Yorba Linda, CA 92887-4610 USA
www.globalspecialties.com



